

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

This is a pre-release version of the X server from The X.Org Foundation.
It is not supported in any way.
Bugs may be filed in the bugzilla at <http://bugs.freedesktop.org/>.
Select the "xorg" product for bugs you find in this release.
Before reporting bugs in pre-release versions please check the
latest version in the X.Org Foundation git repository.
See <http://wiki.x.org/wiki/GitPage> for git access instructions.

X.Org X Server 1.4.0.90
Release Date: 5 September 2007
X Protocol Version 11, Revision 0
Build Operating System: Linux Ubuntu (xorg-server 2:1.4.1~git20080118-1ubuntu2)
Current Operating System: Linux daisy 2.6.24-5-generic #1 SMP Thu Jan 24 19:45:21 UTC 2008 i686
Build Date: 19 January 2008 07:43:36PM

Before reporting problems, check <http://wiki.x.org>
to make sure that you have the latest version.

Module Loader present

Markers: (--) probed, (**) from config file, (==) default setting,
(++) from command line, (!!) notice, (II) informational,
(WW) warning, (EE) error, (NI) not implemented, (??) unknown.

(==) Log file: "/var/log/Xorg.0.log", Time: Sat Feb 2 18:32:55 2008
(==) Using config file: "/etc/X11/xorg.conf"
(==) No Layout section. Using the first Screen section.
(**) |-->Screen "Default Screen" (0)
(**) | |-->Monitor "Philips 190B"
(**) | |-->Device "nVidia Corporation NV34"
(==) |-->Input Device "<default pointer>"
(==) |-->Input Device "<default keyboard>"
(==) The core pointer device wasn't specified explicitly in the layout.
Using the default mouse configuration.
(==) The core keyboard device wasn't specified explicitly in the layout.
Using the default keyboard configuration.
(==) Automatically adding devices
(==) Automatically enabling devices
(==) No FontPath specified. Using compiled-in default.

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```
(WW) The directory "/usr/share/fonts/X11/cyrillic" does not exist.
      Entry deleted from font path.
(==) FontPath set to:
      /usr/share/fonts/X11/misc,
      /usr/share/fonts/X11/100dpi:unscaled,
      /usr/share/fonts/X11/75dpi:unscaled,
      /usr/share/fonts/X11/Type1,
      /usr/share/fonts/X11/100dpi,
      /usr/share/fonts/X11/75dpi,
      /var/lib/defoma/x-ttcidfont-conf.d/dirs/TrueType
(==) RgbPath set to "/etc/X11/rgb"
(==) ModulePath set to "/usr/lib/xorg/modules"
(II) Open ACPI successful (/var/run/acpid.socket)
(II) Loader magic: 0x81dcd40
(II) Module ABI versions:
      X.Org ANSI C Emulation: 0.3
      X.Org Video Driver: 2.0
      X.Org XInput driver : 2.0
      X.Org Server Extension : 0.3
      X.Org Font Renderer : 0.5
(II) Loader running on linux
(II) LoadModule: "pcidata"
(II) Loading /usr/lib/xorg/modules//libpcidata.so
(II) Module pcidata: vendor="X.Org Foundation"
      compiled for 1.4.0.90, module version = 1.0.0
      ABI class: X.Org Video Driver, version 2.0
(-- ) using VT number 7

(II) PCI: PCI scan (all values are in hex)
(II) PCI: 00:00:0: chip 8086,7190 card 0000,0000 rev 03 class 06,00,00 hdr 00
(II) PCI: 00:01:0: chip 8086,7191 card 0000,0000 rev 03 class 06,04,00 hdr 01
(II) PCI: 00:06:0: chip 125d,1969 card 125d,8888 rev 01 class 04,01,00 hdr 00
(II) PCI: 00:07:0: chip 8086,7110 card 0000,0000 rev 02 class 06,01,00 hdr 80
(II) PCI: 00:07:1: chip 8086,7111 card 0000,0000 rev 01 class 01,01,80 hdr 00
(II) PCI: 00:07:2: chip 8086,7112 card 0000,0000 rev 01 class 0c,03,00 hdr 00
(II) PCI: 00:07:3: chip 8086,7113 card 0000,0000 rev 02 class 06,80,00 hdr 00
(II) PCI: 00:09:0: chip 10b7,9000 card 0000,0000 rev 00 class 02,00,00 hdr 00
```

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```
(II) PCI: 01:00:0: chip 10de,017a card 10de,0146 rev a3 class 03,00,00 hdr 00
(II) PCI: End of PCI scan
(II) Host-to-PCI bridge:
(II) Bus 0: bridge is at (0:0:0), (0,0,1), BCTRL: 0x0008 (VGA_EN is set)
(II) Bus 0 I/O range:
    [0] -1 0      0x00000000 - 0x0000ffff (0x10000) IX[B]
(II) Bus 0 non-prefetchable memory range:
    [0] -1 0      0x00000000 - 0xffffffff (0x0) MX[B]
(II) Bus 0 prefetchable memory range:
    [0] -1 0      0x00000000 - 0xffffffff (0x0) MX[B]
(II) PCI-to-PCI bridge:
(II) Bus 1: bridge is at (0:1:0), (0,1,1), BCTRL: 0x0088 (VGA_EN is set)
(II) Bus 1 non-prefetchable memory range:
    [0] -1 0      0xde000000 - 0xdf5fffff (0x1600000) MX[B]
(II) Bus 1 prefetchable memory range:
    [0] -1 0      0xdf700000 - 0xe77fffff (0x8100000) MX[B]
(II) PCI-to-ISA bridge:
(II) Bus -1: bridge is at (0:7:0), (0,-1,-1), BCTRL: 0x0008 (VGA_EN is set)
(-- ) PCI:*(1:0:0) nVidia Corporation NV17GL [Quadro NVS] rev 163, Mem @ 0xde000000/24, 0xe0000000/20
0xdf7e0000/17
(II) Addressable bus resource ranges are
    [0] -1 0      0x00000000 - 0xffffffff (0x0) MX[B]
    [1] -1 0      0x00000000 - 0x0000ffff (0x10000) IX[B]
(II) OS-reported resource ranges:
    [0] -1 0      0x00100000 - 0x3fffffff (0x3ff00000) MX[B]E(B)
    [1] -1 0      0x000f0000 - 0x000fffff (0x10000) MX[B]
    [2] -1 0      0x000c0000 - 0x000effff (0x30000) MX[B]
    [3] -1 0      0x00000000 - 0x0009ffff (0xa0000) MX[B]
    [4] -1 0      0x0000ffff - 0x0000ffff (0x1) IX[B]
    [5] -1 0      0x00000000 - 0x000000ff (0x100) IX[B]
(II) PCI Memory resource overlap reduced 0xe7800000 from 0xe7bfffff to 0xe77fffff
(II) Active PCI resource ranges:
    [0] -1 0      0xe7800000 - 0xe77fffff (0x0) MX[B]0
    [1] -1 0      0xdf7e0000 - 0xdf7fffff (0x20000) MX[B](B)
    [2] -1 0      0xdf800000 - 0xdf87ffff (0x80000) MX[B](B)
    [3] -1 0      0xe0000000 - 0xe3fffffff (0x4000000) MX[B](B)
    [4] -1 0      0xde000000 - 0xdeffffff (0x1000000) MX[B](B)
```

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```
[5] -1 0      0x0000a400 - 0x0000a43f (0x40) IX[B]
[6] -1 0      0x0000a800 - 0x0000a81f (0x20) IX[B]
[7] -1 0      0x0000b000 - 0x0000b00f (0x10) IX[B]
[8] -1 0      0x0000b400 - 0x0000b403 (0x4) IX[B]
[9] -1 0      0x0000b800 - 0x0000b803 (0x4) IX[B]
[10] -1 0     0x0000d000 - 0x0000d00f (0x10) IX[B]
[11] -1 0     0x0000d400 - 0x0000d40f (0x10) IX[B]
[12] -1 0     0x0000d800 - 0x0000d83f (0x40) IX[B]
(II) Active PCI resource ranges after removing overlaps:
[0] -1 0      0xe7800000 - 0xe77ffffff (0x0) MX[B]0
[1] -1 0      0xdf7e0000 - 0xdf7ffffff (0x20000) MX[B](B)
[2] -1 0      0xdf800000 - 0xdf87ffff (0x80000) MX[B](B)
[3] -1 0      0xe0000000 - 0xe3ffffff (0x4000000) MX[B](B)
[4] -1 0      0xde000000 - 0xdeffffff (0x1000000) MX[B](B)
[5] -1 0      0x0000a400 - 0x0000a43f (0x40) IX[B]
[6] -1 0      0x0000a800 - 0x0000a81f (0x20) IX[B]
[7] -1 0      0x0000b000 - 0x0000b00f (0x10) IX[B]
[8] -1 0      0x0000b400 - 0x0000b403 (0x4) IX[B]
[9] -1 0      0x0000b800 - 0x0000b803 (0x4) IX[B]
[10] -1 0     0x0000d000 - 0x0000d00f (0x10) IX[B]
[11] -1 0     0x0000d400 - 0x0000d40f (0x10) IX[B]
[12] -1 0     0x0000d800 - 0x0000d83f (0x40) IX[B]
(II) OS-reported resource ranges after removing overlaps with PCI:
[0] -1 0      0x00100000 - 0x3fffffff (0x3ff00000) MX[B]E(B)
[1] -1 0      0x000f0000 - 0x000fffff (0x10000) MX[B]
[2] -1 0      0x000c0000 - 0x000effff (0x30000) MX[B]
[3] -1 0      0x00000000 - 0x0009ffff (0xa0000) MX[B]
[4] -1 0      0x0000ffff - 0x0000ffff (0x1) IX[B]
[5] -1 0      0x00000000 - 0x000000ff (0x100) IX[B]
(II) All system resource ranges:
[0] -1 0      0x00100000 - 0x3fffffff (0x3ff00000) MX[B]E(B)
[1] -1 0      0x000f0000 - 0x000fffff (0x10000) MX[B]
[2] -1 0      0x000c0000 - 0x000effff (0x30000) MX[B]
[3] -1 0      0x00000000 - 0x0009ffff (0xa0000) MX[B]
[4] -1 0      0xe7800000 - 0xe77ffffff (0x0) MX[B]0
[5] -1 0      0xdf7e0000 - 0xdf7ffffff (0x20000) MX[B](B)
[6] -1 0      0xdf800000 - 0xdf87ffff (0x80000) MX[B](B)
```

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```
[7] -1 0      0xe0000000 - 0xe3ffffff (0x4000000) MX[B] (B)
[8] -1 0      0xde000000 - 0xdeffffff (0x1000000) MX[B] (B)
[9] -1 0      0x0000ffff - 0x0000ffff (0x1) IX[B]
[10] -1 0     0x00000000 - 0x000000ff (0x100) IX[B]
[11] -1 0     0x0000a400 - 0x0000a43f (0x40) IX[B]
[12] -1 0     0x0000a800 - 0x0000a81f (0x20) IX[B]
[13] -1 0     0x0000b000 - 0x0000b00f (0x10) IX[B]
[14] -1 0     0x0000b400 - 0x0000b403 (0x4) IX[B]
[15] -1 0     0x0000b800 - 0x0000b803 (0x4) IX[B]
[16] -1 0     0x0000d000 - 0x0000d00f (0x10) IX[B]
[17] -1 0     0x0000d400 - 0x0000d40f (0x10) IX[B]
[18] -1 0     0x0000d800 - 0x0000d83f (0x40) IX[B]
(II) LoadModule: "extmod"
(II) Loading /usr/lib/xorg/modules/extensions//libextmod.so
(II) Module extmod: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 1.0.0
        Module class: X.Org Server Extension
        ABI class: X.Org Server Extension, version 0.3
(II) Loading extension SHAPE
(II) Loading extension MIT-SUNDRY-NONSTANDARD
(II) Loading extension BIG-REQUESTS
(II) Loading extension SYNC
(II) Loading extension MIT-SCREEN-SAVER
(II) Loading extension XC-MISC
(II) Loading extension XFree86-VidModeExtension
(II) Loading extension XFree86-Misc
(II) Loading extension XFree86-DGA
(II) Loading extension DPMS
(II) Loading extension TOG-CUP
(II) Loading extension Extended-Visual-Information
(II) Loading extension XVideo
(II) Loading extension XVideo-MotionCompensation
(II) Loading extension X-Resource
(II) LoadModule: "dbe"
(II) Loading /usr/lib/xorg/modules/extensions//libdbe.so
(II) Module dbbe: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 1.0.0
```

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```
Module class: X.Org Server Extension
ABI class: X.Org Server Extension, version 0.3
(II) Loading extension DOUBLE-BUFFER
(II) LoadModule: "glx"
(II) Loading /usr/lib/xorg/modules/extensions//libglx.so
(II) Module glx: vendor="X.Org Foundation"
    compiled for 1.4.0.90, module version = 1.0.0
    ABI class: X.Org Server Extension, version 0.3
(==) AIGLX enabled
(II) Loading extension GLX
(II) LoadModule: "freetype"
(II) Loading /usr/lib/xorg/modules//fonts/libfreetype.so
(II) Module freetype: vendor="X.Org Foundation & the After X-TT Project"
    compiled for 1.4.0.90, module version = 2.1.0
    Module class: X.Org Font Renderer
    ABI class: X.Org Font Renderer, version 0.5
(II) Loading font FreeType
(II) LoadModule: "record"
(II) Loading /usr/lib/xorg/modules/extensions//librecord.so
(II) Module record: vendor="X.Org Foundation"
    compiled for 1.4.0.90, module version = 1.13.0
    Module class: X.Org Server Extension
    ABI class: X.Org Server Extension, version 0.3
(II) Loading extension RECORD
(II) LoadModule: "dri"
(II) Loading /usr/lib/xorg/modules/extensions//libdri.so
(II) Module dri: vendor="X.Org Foundation"
    compiled for 1.4.0.90, module version = 1.0.0
    ABI class: X.Org Server Extension, version 0.3
(II) Loading extension XFree86-DRI
(II) LoadModule: "nouveau"
(II) Loading /usr/lib/xorg/modules/drivers//nouveau_drv.so
(II) Module nouveau: vendor="X.Org Foundation"
    compiled for 1.4.0.90, module version = 0.0.10
    Module class: X.Org Video Driver
    ABI class: X.Org Video Driver, version 2.0
(II) LoadModule: "mouse"
```

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```
(II) Loading /usr/lib/xorg/modules/input//mouse_drv.so
(II) Module mouse: vendor="X.Org Foundation"
        compiled for 1.4.0, module version = 1.2.3
        Module class: X.Org XInput Driver
        ABI class: X.Org XInput driver, version 2.0
(II) LoadModule: "kbd"
(II) Loading /usr/lib/xorg/modules/input//kbd_drv.so
(II) Module kbd: vendor="X.Org Foundation"
        compiled for 1.4.0, module version = 1.2.2
        Module class: X.Org XInput Driver
        ABI class: X.Org XInput driver, version 2.0
(II) NOUVEAU driver Date:   Sat Feb 2 04:04:58 2008 +0100
(II) NOUVEAU driver for NVIDIA chipset families :
        RIVA TNT      (NV04)
        RIVA TNT2    (NV05)
        GeForce 256  (NV10)
        GeForce 2     (NV11, NV15)
        GeForce 4MX  (NV17, NV18)
        GeForce 3     (NV20)
        GeForce 4Ti  (NV25, NV28)
        GeForce FX   (NV3x)
        GeForce 6    (NV4x)
        GeForce 7    (G7x)
        GeForce 8    (G8x)
(II) Primary Device is: PCI 01:00:0
(--) Chipset NVIDIA NV17 found
(II) resource ranges after xf86ClaimFixedResources() call:
    [0] -1 0           0x00100000 - 0x3fffffff (0x3ff00000) MX[B]E(B)
    [1] -1 0           0x000f0000 - 0x000fffff (0x10000) MX[B]
    [2] -1 0           0x000c0000 - 0x000effff (0x30000) MX[B]
    [3] -1 0           0x00000000 - 0x0009ffff (0xa0000) MX[B]
    [4] -1 0           0xe7800000 - 0xe77fffff (0x0) MX[B]0
    [5] -1 0           0xdf7e0000 - 0xdf7fffff (0x20000) MX[B](B)
    [6] -1 0           0xdf800000 - 0xdf87ffff (0x80000) MX[B](B)
    [7] -1 0           0xe0000000 - 0xe3ffffff (0x4000000) MX[B](B)
    [8] -1 0           0xde000000 - 0xdeffffff (0x1000000) MX[B](B)
    [9] -1 0           0x0000ffff - 0x0000ffff (0x1) IX[B]
```

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```
[10] -1 0      0x00000000 - 0x000000ff (0x100) IX[B]
[11] -1 0      0x0000a400 - 0x0000a43f (0x40) IX[B]
[12] -1 0      0x0000a800 - 0x0000a81f (0x20) IX[B]
[13] -1 0      0x0000b000 - 0x0000b00f (0x10) IX[B]
[14] -1 0      0x0000b400 - 0x0000b403 (0x4) IX[B]
[15] -1 0      0x0000b800 - 0x0000b803 (0x4) IX[B]
[16] -1 0      0x0000d000 - 0x0000d00f (0x10) IX[B]
[17] -1 0      0x0000d400 - 0x0000d40f (0x10) IX[B]
[18] -1 0      0x0000d800 - 0x0000d83f (0x40) IX[B]
```

(II) resource ranges after probing:

```
[0] -1 0      0x00100000 - 0x3fffffff (0x3ff00000) MX[B]E(B)
[1] -1 0      0x000f0000 - 0x000fffff (0x10000) MX[B]
[2] -1 0      0x000c0000 - 0x000effff (0x30000) MX[B]
[3] -1 0      0x00000000 - 0x0009ffff (0xa0000) MX[B]
[4] -1 0      0xe7800000 - 0xe77fffff (0x0) MX[B]0
[5] -1 0      0xdf7e0000 - 0xdf7fffff (0x20000) MX[B](B)
[6] -1 0      0xdf800000 - 0xdf87fffff (0x80000) MX[B](B)
[7] -1 0      0xe0000000 - 0xe3fffff (0x4000000) MX[B](B)
[8] -1 0      0xde000000 - 0xdeffffff (0x1000000) MX[B](B)
[9] 0 0        0x000a0000 - 0x000affff (0x10000) MS[B]
[10] 0 0       0x000b0000 - 0x000b7fff (0x8000) MS[B]
[11] 0 0       0x000b8000 - 0x000bffff (0x8000) MS[B]
[12] -1 0      0x0000ffff - 0x0000ffff (0x1) IX[B]
[13] -1 0      0x00000000 - 0x000000ff (0x100) IX[B]
[14] -1 0      0x0000a400 - 0x0000a43f (0x40) IX[B]
[15] -1 0      0x0000a800 - 0x0000a81f (0x20) IX[B]
[16] -1 0      0x0000b000 - 0x0000b00f (0x10) IX[B]
[17] -1 0      0x0000b400 - 0x0000b403 (0x4) IX[B]
[18] -1 0      0x0000b800 - 0x0000b803 (0x4) IX[B]
[19] -1 0      0x0000d000 - 0x0000d00f (0x10) IX[B]
[20] -1 0      0x0000d400 - 0x0000d40f (0x10) IX[B]
[21] -1 0      0x0000d800 - 0x0000d83f (0x40) IX[B]
[22] 0 0       0x000003b0 - 0x000003bb (0xc) IS[B]
[23] 0 0       0x000003c0 - 0x000003df (0x20) IS[B]
```

(II) Setting vga for screen 0.
(II) Loading sub module "int10"
(II) LoadModule: "int10"

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```
(II) Loading /usr/lib/xorg/modules//libint10.so
(II) Module int10: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 1.0.0
        ABI class: X.Org Video Driver, version 2.0
(II) NOUVEAU(0): Initializing int10
(II) NOUVEAU(0): Primary V_BIOS segment is: 0xc000
(-- ) NOUVEAU(0): Chipset: "NVIDIA NV17"
(**) NOUVEAU(0): Depth 24, (-- ) framebuffer bpp 32
(==) NOUVEAU(0): RGB weight 888
(==) NOUVEAU(0): Default visual is TrueColor
(II) Loading sub module "vgahw"
(II) LoadModule: "vgahw"
(II) Loading /usr/lib/xorg/modules//libvgahw.so
(II) Module vgahw: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 0.1.0
        ABI class: X.Org Video Driver, version 2.0
(**) NOUVEAU(0): Option "Randr12" "true"
(==) NOUVEAU(0): Randr1.2 support enabled
(==) NOUVEAU(0): New (experimental) restore support disabled
(==) NOUVEAU(0): Using HW cursor
(-- ) NOUVEAU(0): Linear framebuffer at 0xE0000000
(-- ) NOUVEAU(0): MMIO registers at 0xDE000000
(II) Loading sub module "dri"
(II) LoadModule: "dri"
(II) Reloading /usr/lib/xorg/modules/extensions//libdri.so
(II) NOUVEAU(0): Loaded DRI module
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 7, (OK)
drmOpenByBusid: Searching for BusID pci:0000:01:00.0
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 7, (OK)
drmOpenByBusid: drmOpenMinor returns 7
drmOpenByBusid: drmGetBusid reports pci:0000:01:00.0
(II) NOUVEAU(0): [dri] Found DRI library version 1.3.0 and kernel module version 0.0.10
Initial CRTC_OWNER is 7
(II) NOUVEAU(0): Attempting to locate BIOS image in PROM
(II) NOUVEAU(0): ... appears to be valid
```

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```
(II) NOUVEAU(0):      Read: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Read: Reg: 0x00100200, Data: 0x08C10010
(II) NOUVEAU(0):      Write: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x1F, Head: 0x00, Data: 0x57
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x11, Head: 0x00, Data: 0x8E
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x11, Head: 0x00, Data: 0x0E
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x03
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x1F, Head: 0x01, Data: 0x57
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x11, Head: 0x01, Data: 0x8E
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x11, Head: 0x01, Data: 0x0E
(II) NOUVEAU(0): Loading NV17 power sequencing microcode
(II) NOUVEAU(0):      Write: Reg: 0x00001304, Data: 0x00000800
(II) NOUVEAU(0):      Write: Reg: 0x00001400, Data: 0xA9A805A2
(II) NOUVEAU(0):      Write: Reg: 0x00001404, Data: 0x7FA0CD03
(II) NOUVEAU(0):      Write: Reg: 0x00001408, Data: 0xC9C80BC0
(II) NOUVEAU(0):      Write: Reg: 0x0000140C, Data: 0x7F29C205
(II) NOUVEAU(0):      Write: Reg: 0x00001410, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00001414, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00001418, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x0000141C, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x00001098, Data: 0x20062E7C
(II) NOUVEAU(0):      Write: Reg: 0x00001098, Data: 0x20062E7C
(II) NOUVEAU(0): BMP BIOS found
(II) NOUVEAU(0): BMP version 5.21
(II) NOUVEAU(0): Bios version 04.17.00.55
(II) NOUVEAU(0): 0xCE28: Parsing init table 0
(II) NOUVEAU(0): 0xCE28: ----- EXECUTING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xCE28: [ (0x65) - INIT_RESET ]
(II) NOUVEAU(0):      Read: Reg: 0x0000184C, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x0000184C, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00000200, Data: 0x03000000
(II) NOUVEAU(0):      Write: Reg: 0x00000200, Data: 0x17111112
(II) NOUVEAU(0):      Write: Reg: 0x0000184C, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x00001850, Data: 0x00000001
(II) NOUVEAU(0):      Write: Reg: 0x00001850, Data: 0x00000000
(II) NOUVEAU(0): 0xCE35: [ (0x6E) - INIT_NV_REG ]
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0): 0xCE35: Reg: 0x00101000, Mask: 0xFFFFDFFF, Data: 0x80000400
(II) NOUVEAU(0):      Read: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Write: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0): 0xCE42: [ (0x75) - INIT_CONDITION ]
(II) NOUVEAU(0): 0xCE42: Cond: 0x00, Reg: 0x00101000, Mask: 0x00000040, Cmpval: 0x00000040
(II) NOUVEAU(0):      Read: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0): 0xCE42: Checking if 0x00000040 equals 0x00000040
(II) NOUVEAU(0): 0xCE42: CONDITION FULFILLED - CONTINUING TO EXECUTE
(II) NOUVEAU(0): 0xCE44: [ (0x62) - INIT_ZM_INDEX_IO ]
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x01
(II) NOUVEAU(0): 0xCE49: [ (0x38) - INIT_NOT ]
(II) NOUVEAU(0): 0xCE49: ----- SKIPPING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xCE4A: [ (0x62) - INIT_ZM_INDEX_IO ]
(II) NOUVEAU(0): 0xCE4F: [ (0x72) - INIT_RESUME ]
(II) NOUVEAU(0): 0xCE4F: ---- EXECUTING FOLLOWING COMMANDS ----
(II) NOUVEAU(0): 0xCE50: [ (0x37) - INIT_COPY ]
(II) NOUVEAU(0): 0xCE50: Reg: 0x00101000, Shift: 0xFE, SrcMask: 0xF0, Port: 0x03D4, Index: 0x3C, Mask: 0x00000000, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x10
(II) NOUVEAU(0): 0xCE5B: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCE5B: Port: 0x03D4, Index: 0x35, Mask: 0xFD, Data: 0x01
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x35, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x35, Head: 0x00, Data: 0x01
(II) NOUVEAU(0): 0xCE61: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCE61: Port: 0x03D4, Index: 0x3C, Mask: 0xF7, Data: 0x08
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x08
(II) NOUVEAU(0): 0xCE67: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00000200, Data: 0x17011113
(II) NOUVEAU(0): 0xCE70: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCE70: Port: 0x03C4, Index: 0x01, Mask: 0xFF, Data: 0x20
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03C4, Index: 0x01, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03C4, Index: 0x01, Head: 0x00, Data: 0x20
(II) NOUVEAU(0): 0xCE76: [ (0x79) - INIT_PLL ]
(II) NOUVEAU(0): 0xCE76: Reg: 0x00680500, Freq: 100000kHz
(II) NOUVEAU(0):      Read: Reg: 0x00101000, Data: 0xA0A0E5C7
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0):      Read:  Reg: 0x00680504, Data: 0x00004603
(II) NOUVEAU(0):      Read:  Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000010
(II) NOUVEAU(0):      Write: Reg: 0x00680504, Data: 0x0000FB09
(II) NOUVEAU(0):      Read:  Reg: 0x00680504, Data: 0x00004603
(II) NOUVEAU(0):      Write: Reg: 0x00680504, Data: 0x0002FB09
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0): 0xCE7D: [ (0x39) - INIT_IO_FLAG_CONDITION ]
(II) NOUVEAU(0): 0xCE7D: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, FlagArray: 0xCE18, FAM
(II) NOUVEAU(0):      Indexed IO read:  Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCE7D: Checking if 0x00 equals 0x00
(II) NOUVEAU(0): 0xCE7D: CONDITION FULFILLED - CONTINUING TO EXECUTE
(II) NOUVEAU(0): 0xCE7F: [ (0x79) - INIT_PLL ]
(II) NOUVEAU(0): 0xCE7F: Reg: 0x00680504, Freq: 20000kHz
(II) NOUVEAU(0):      Read:  Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Read:  Reg: 0x00680508, Data: 0x0004DB07
(II) NOUVEAU(0):      Read:  Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000100
(II) NOUVEAU(0):      Write: Reg: 0x00680508, Data: 0x0004F60B
(II) NOUVEAU(0):      Read:  Reg: 0x00680508, Data: 0x0004DB07
(II) NOUVEAU(0):      Write: Reg: 0x00680508, Data: 0x0004F60B
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0): 0xCE86: [ (0x38) - INIT_NOT ]
(II) NOUVEAU(0): 0xCE86: ----- SKIPPING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xCE87: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCE94: [ (0x79) - INIT_PLL ]
(II) NOUVEAU(0): 0xCE9B: [ (0x72) - INIT_RESUME ]
(II) NOUVEAU(0): 0xCE9B: ---- EXECUTING FOLLOWING COMMANDS ----
(II) NOUVEAU(0): 0xCE9C: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCE9C: Reg: 0x000010B0, Mask: 0x0000000F, Data: 0x0413021A
(II) NOUVEAU(0):      Read:  Reg: 0x000010B0, Data: 0x0413021A
(II) NOUVEAU(0):      Write: Reg: 0x000010B0, Data: 0x0413021A
(II) NOUVEAU(0): 0xCEA9: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCEA9: Reg: 0x000010B4, Mask: 0x00000000, Data: 0xEE000000
(II) NOUVEAU(0):      Read:  Reg: 0x000010B4, Data: 0xEE000000
(II) NOUVEAU(0):      Write: Reg: 0x000010B4, Data: 0xEE000000
(II) NOUVEAU(0): 0xCEB6: [ (0x6E) - INIT_NV_REG ]
```

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```
(II) NOUVEAU(0): 0xCEB6: Reg: 0x000010B8, Mask: 0x00000000, Data: 0x01013131
(II) NOUVEAU(0):      Read: Reg: 0x000010B8, Data: 0x01013131
(II) NOUVEAU(0):      Write: Reg: 0x000010B8, Data: 0x01013131
(II) NOUVEAU(0): 0xCEC3: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCEC3: Reg: 0x000010BC, Mask: 0x0000000F, Data: 0xFF010000
(II) NOUVEAU(0):      Read: Reg: 0x000010BC, Data: 0xFF010003
(II) NOUVEAU(0):      Write: Reg: 0x000010BC, Data: 0xFF010003
(II) NOUVEAU(0): 0xCED0: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCED0: Reg: 0x000010CC, Mask: 0x00000000, Data: 0x00000022
(II) NOUVEAU(0):      Read: Reg: 0x000010CC, Data: 0x00000022
(II) NOUVEAU(0):      Write: Reg: 0x000010CC, Data: 0x00000022
(II) NOUVEAU(0): 0xCEDD: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCEDD: Reg: 0x000010D4, Mask: 0xFF00000F, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x000010D4, Data: 0x1C000000
(II) NOUVEAU(0):      Write: Reg: 0x000010D4, Data: 0x1C000000
(II) NOUVEAU(0): 0xCEEA: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCEEA: Reg: 0x00001580, Mask: 0xFFFFFEFF, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x00001580, Data: 0x200B0000
(II) NOUVEAU(0):      Write: Reg: 0x00001580, Data: 0x200B0000
(II) NOUVEAU(0): 0xCEF7: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x000010E8, Data: 0x00011103
(II) NOUVEAU(0): 0xCF00: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xCF00: Sleeping for 0x0002 microseconds
(II) NOUVEAU(0): 0xCF03: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x0068050C, Data: 0x00000700
(II) NOUVEAU(0): 0xCF0C: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x0068050C, Data: 0x00000500
(II) NOUVEAU(0): 0xCF15: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00000140, Data: 0x00000000
(II) NOUVEAU(0): 0xCF1E: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00000200, Data: 0x17111113
(II) NOUVEAU(0): 0xCF27: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x000010E0, Data: 0x80808080
(II) NOUVEAU(0): 0xCF30: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x000010E4, Data: 0x00808080
(II) NOUVEAU(0): 0xCF39: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00001084, Data: 0x00205749
```

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```
(II) NOUVEAU(0): 0xCF42: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0): 0xD16E: Parsing init table 1
(II) NOUVEAU(0): 0xD16E: ----- EXECUTING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xD16E: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x001002E0, Data: 0x00000000
(II) NOUVEAU(0): 0xD177: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD177: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD177: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00001080, Data: 0x00000000
(II) NOUVEAU(0): 0xD1C2: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD1C2: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD1C2: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00100210, Data: 0x80000000
(II) NOUVEAU(0): 0xD20D: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD20D: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD20D: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00100200, Data: 0x08B10010
(II) NOUVEAU(0): 0xD258: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD258: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD258: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00100220, Data: 0x44090C0C
(II) NOUVEAU(0): 0xD2A3: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD2A3: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD2A3: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00100224, Data: 0x03322260
(II) NOUVEAU(0): 0xD2EE: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD2EE: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD2EE: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00100228, Data: 0x000009FF
(II) NOUVEAU(0): 0xD339: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x000010C4, Data: 0x3333A2A2
```

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```
(II) NOUVEAU(0): 0xD342: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD342: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD342: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x000010C8, Data: 0x66676199
(II) NOUVEAU(0): 0xD38D: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001214, Data: 0x88888888
(II) NOUVEAU(0): 0xD396: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001210, Data: 0x00000000
(II) NOUVEAU(0): 0xD39F: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x0000122C, Data: 0xFFFFFFFF
(II) NOUVEAU(0): 0xD3A8: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x0000123C, Data: 0xFFFFFFFF
(II) NOUVEAU(0): 0xD3B1: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001234, Data: 0x44444444
(II) NOUVEAU(0): 0xD3BA: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001240, Data: 0xF2F2F2F2
(II) NOUVEAU(0): 0xD3C3: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001248, Data: 0xAAAAAAAA
(II) NOUVEAU(0): 0xD3CC: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001244, Data: 0xFFFFFFFF
(II) NOUVEAU(0): 0xD3D5: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001230, Data: 0xFFFFFFFF
(II) NOUVEAU(0): 0xD3DE: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00001238, Data: 0x33333333
(II) NOUVEAU(0): 0xD3E7: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD3E7: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD3E7: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00001218, Data: 0xF0000100
(II) NOUVEAU(0): 0xD432: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD432: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD432: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x000010D8, Data: 0x00007777
(II) NOUVEAU(0): 0xD47D: [ (0x39) - INIT_IO_FLAG_CONDITION ]
(II) NOUVEAU(0): 0xD47D: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, FlagArray: 0xCE18, FAM
```

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```
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD47D: Checking if 0x00 equals 0x00
(II) NOUVEAU(0): 0xD47D: CONDITION FULFILLED - CONTINUING TO EXECUTE
(II) NOUVEAU(0): 0xD47F: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD47F: Macro: 0x00, MacroTableIndex: 0x00, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D4, Data: 0x00000001
(II) NOUVEAU(0): 0xD481: [ (0x72) - INIT_RESUME ]
(II) NOUVEAU(0): 0xD482: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x00100218, Data: 0x00000100
(II) NOUVEAU(0): 0xD48B: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0): Write: Reg: 0x0010021C, Data: 0x00000001
(II) NOUVEAU(0): 0xD494: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD494: Sleeping for 0x00C8 microseconds
(II) NOUVEAU(0): 0xD497: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD497: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD497: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x00100218, Data: 0x00000101
(II) NOUVEAU(0): 0xD4E2: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD4E2: Sleeping for 0x0002 microseconds
(II) NOUVEAU(0): 0xD4E5: [ (0x39) - INIT_IO_FLAG_CONDITION ]
(II) NOUVEAU(0): 0xD4E5: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, FlagArray: 0xCE18, FAM
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD4E5: Checking if 0x00 equals 0x00
(II) NOUVEAU(0): 0xD4E5: CONDITION FULFILLED - CONTINUING TO EXECUTE
(II) NOUVEAU(0): 0xD4E7: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD4E7: Macro: 0x01, MacroTableIndex: 0x01, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x0010021C, Data: 0x00000001
(II) NOUVEAU(0): 0xD4E9: [ (0x38) - INIT_NOT ]
(II) NOUVEAU(0): 0xD4E9: ----- SKIPPING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xD4EA: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD4EC: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD4EE: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD4F1: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD53C: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD53F: [ (0x72) - INIT_RESUME ]
(II) NOUVEAU(0): 0xD53F: ---- EXECUTING FOLLOWING COMMANDS ----
```


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(II) NOUVEAU(0): 0xD540: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD540: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD540: Writing config 00
(II) NOUVEAU(0): Write: Reg: 0x001002C0, Data: 0x00000030
(II) NOUVEAU(0): 0xD58B: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD58B: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000100
(II) NOUVEAU(0): Read: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): Write: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): 0xD598: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD598: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): Write: Reg: 0x001002C0, Data: 0x00000030
(II) NOUVEAU(0): 0xD5A5: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD5A5: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000100
(II) NOUVEAU(0): Read: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): Write: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): 0xD5B2: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD5B2: Sleeping for 0x07D0 microseconds
(II) NOUVEAU(0): 0xD5B5: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5B5: Macro: 0x00, MacroTableIndex: 0x00, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D4, Data: 0x00000001
(II) NOUVEAU(0): 0xD5B7: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD5B7: Sleeping for 0x07D0 microseconds
(II) NOUVEAU(0): 0xD5BA: [ (0x39) - INIT_IO_FLAG_CONDITION ]
(II) NOUVEAU(0): 0xD5BA: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, FlagArray: 0xCE18, FAM
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD5BA: Checking if 0x00 equals 0x00
(II) NOUVEAU(0): 0xD5BA: CONDITION FULFILLED - CONTINUING TO EXECUTE
(II) NOUVEAU(0): 0xD5BC: [ (0x33) - INIT_REPEAT ]
(II) NOUVEAU(0): 0xD5BC: REPEATING FOLLOWING SEGMENT 8 TIMES
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
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(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5BE: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5BE: Macro: 0x02, MacroTableIndex: 0x02, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D0, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C0: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5C1: [ (0x33) - INIT_REPEAT ]
(II) NOUVEAU(0): 0xD5C1: REPEATING FOLLOWING SEGMENT 3 TIMES
(II) NOUVEAU(0): 0xD5C3: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5C3: Macro: 0x00, MacroTableIndex: 0x00, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D4, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C5: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5C3: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5C3: Macro: 0x00, MacroTableIndex: 0x00, Count: 0x01
(II) NOUVEAU(0): Write: Reg: 0x001002D4, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C5: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5C3: [ (0x6F) - INIT_MACRO ]
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(II) NOUVEAU(0): 0xD5C3: Macro: 0x00, MacroTableIndex: 0x00, Count: 0x01
(II) NOUVEAU(0):      Write: Reg: 0x001002D4, Data: 0x00000001
(II) NOUVEAU(0): 0xD5C5: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5C6: [ (0x38) - INIT_NOT ]
(II) NOUVEAU(0): 0xD5C6: ----- SKIPPING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xD5C7: [ (0x33) - INIT_REPEAT ]
(II) NOUVEAU(0): 0xD5C7: REPEATING FOLLOWING SEGMENT 1 TIMES
(II) NOUVEAU(0): 0xD5C9: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5CB: [ (0x36) - INIT_END_REPEAT ]
(II) NOUVEAU(0): 0xD5CC: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5CE: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD5D1: [ (0x6F) - INIT_MACRO ]
(II) NOUVEAU(0): 0xD5D3: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD5D6: [ (0x72) - INIT_RESUME ]
(II) NOUVEAU(0): 0xD5D6: ---- EXECUTING FOLLOWING COMMANDS ----
(II) NOUVEAU(0): 0xD5D7: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD5D7: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD5D7: Writing config 00
(II) NOUVEAU(0):      Write: Reg: 0x001002C0, Data: 0x00000030
(II) NOUVEAU(0): 0xD622: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD622: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000100
(II) NOUVEAU(0):      Read: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0):      Write: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): 0xD62F: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD62F: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0):      Write: Reg: 0x001002C0, Data: 0x00000030
(II) NOUVEAU(0): 0xD63C: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD63C: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000100
(II) NOUVEAU(0):      Read: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0):      Write: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): 0xD649: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD649: Sleeping for 0x07D0 microseconds
(II) NOUVEAU(0): 0xD64C: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD64C: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
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(II) NOUVEAU(0): 0xD64C: Writing config 00
(II) NOUVEAU(0):      Write: Reg: 0x00100210, Data: 0x80000000
(II) NOUVEAU(0): 0xD697: [ (0x32) - INIT_IO_RESTRICT_PROG ]
(II) NOUVEAU(0): 0xD697: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, Count: 0x10, Reg: 0x00
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD697: Writing config 00
(II) NOUVEAU(0):      Write: Reg: 0x000010D4, Data: 0x00000000
(II) NOUVEAU(0): 0xD6E2: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD6E2: Reg: 0x00100080, Mask: 0x00000000, Data: 0x00020000
(II) NOUVEAU(0):      Read: Reg: 0x00100080, Data: 0x00020000
(II) NOUVEAU(0):      Write: Reg: 0x00100080, Data: 0x00020000
(II) NOUVEAU(0): 0xD6EF: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD6EF: Reg: 0x00100218, Mask: 0xFFFFFEFF, Data: 0x00000000
(II) NOUVEAU(0):      Read: Reg: 0x00100218, Data: 0x00000001
(II) NOUVEAU(0):      Write: Reg: 0x00100218, Data: 0x00000001
(II) NOUVEAU(0): 0xD6FC: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0): 0xCF43: Parsing init table 2
(II) NOUVEAU(0): 0xCF43: ----- EXECUTING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xCF43: [ (0x63) - INIT_COMPUTE_MEM ]
(II) NOUVEAU(0):      Write: Reg: 0x00100210, Data: 0x80000000
(II) NOUVEAU(0):      Write: Reg: 0x00100200, Data: 0x08C10010
(II) NOUVEAU(0): 0xCF44: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00001088, Data: 0x000011F0
(II) NOUVEAU(0): 0xCF4D: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x0000108C, Data: 0x000000D1
(II) NOUVEAU(0): 0xCF56: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xCF56: Reg: 0x00001098, Mask: 0xFFFFFFFF, Data: 0x00040000
(II) NOUVEAU(0):      Read: Reg: 0x00001098, Data: 0x20062E7C
(II) NOUVEAU(0):      Write: Reg: 0x00001098, Data: 0x20062E7C
(II) NOUVEAU(0): 0xCF63: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF63: Port: 0x03D4, Index: 0x38, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x38, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x38, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF69: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF69: Port: 0x03D4, Index: 0x1A, Mask: 0x00, Data: 0x3F
(II) NOUVEAU(0):      Indexed IO read: Port: 0x03D4, Index: 0x1A, Head: 0x00, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x1A, Head: 0x00, Data: 0x3F
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(II) NOUVEAU(0): 0xCF6F: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF6F: Port: 0x03D4, Index: 0x28, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x28, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x28, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF75: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF75: Port: 0x03D4, Index: 0x33, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x33, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x33, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF7B: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF7B: Port: 0x03D4, Index: 0x21, Mask: 0x00, Data: 0xFA
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x21, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x21, Head: 0x00, Data: 0xFA
(II) NOUVEAU(0): 0xCF81: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF81: Port: 0x03D4, Index: 0x2B, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x2B, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x2B, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF87: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF87: Port: 0x03D4, Index: 0x2C, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x2C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x2C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF8D: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF8D: Port: 0x03D4, Index: 0x3A, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3A, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x3A, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF93: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF93: Port: 0x03D4, Index: 0x3B, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3B, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x3B, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF99: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF99: Port: 0x03D4, Index: 0x4A, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x4A, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x4A, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF9F: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF9F: Port: 0x03D4, Index: 0x4B, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x4B, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x4B, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCFA5: [ (0x78) - INIT_INDEX_IO ]
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(II) NOUVEAU(0): 0xCF A5: Port: 0x03D4, Index: 0x4C, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x4C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x4C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCF AB: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF AB: Port: 0x03D4, Index: 0x43, Mask: 0x00, Data: 0x01
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x43, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x43, Head: 0x00, Data: 0x01
(II) NOUVEAU(0): 0xCF B1: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF B1: Port: 0x03D4, Index: 0x44, Mask: 0x00, Data: 0x03
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x07
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x03
(II) NOUVEAU(0): 0xCF B7: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF B7: Port: 0x03D4, Index: 0x21, Mask: 0x00, Data: 0xFA
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x21, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x21, Head: 0x01, Data: 0xFA
(II) NOUVEAU(0): 0xCF BD: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF BD: Port: 0x03D4, Index: 0x2B, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x2B, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x2B, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCF C3: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF C3: Port: 0x03D4, Index: 0x2C, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x2C, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x2C, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCF C9: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF C9: Port: 0x03D4, Index: 0x3A, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3A, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x3A, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCF CF: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF CF: Port: 0x03D4, Index: 0x3B, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3B, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x3B, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCF D5: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF D5: Port: 0x03D4, Index: 0x3C, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x3C, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCF DB: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCF DB: Port: 0x03D4, Index: 0x4A, Mask: 0x00, Data: 0x00
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(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x4A, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x4A, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCFE1: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCFE1: Port: 0x03D4, Index: 0x4B, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x4B, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x4B, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCFE7: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCFE7: Port: 0x03D4, Index: 0x4C, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x4C, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x4C, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): 0xCFED: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCFED: Port: 0x03D4, Index: 0x43, Mask: 0x00, Data: 0x01
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x43, Head: 0x01, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x43, Head: 0x01, Data: 0x01
(II) NOUVEAU(0): 0xCFF3: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCFF3: Port: 0x03D4, Index: 0x44, Mask: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x44, Head: 0x01, Data: 0x07
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xCFF9: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCFF9: Port: 0x03D4, Index: 0x52, Mask: 0x00, Data: 0x04
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x52, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x52, Head: 0x00, Data: 0x04
(II) NOUVEAU(0): 0xCFFF: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xCFFF: Port: 0x03D4, Index: 0x53, Mask: 0x00, Data: 0x40
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x53, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x53, Head: 0x00, Data: 0x40
(II) NOUVEAU(0): 0xD005: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD005: Reg: 0x0060081C, Mask: 0xFFCFFFFFFF, Data: 0x80100000
(II) NOUVEAU(0): Read: Reg: 0x0060081C, Data: 0xA2566656
(II) NOUVEAU(0): Write: Reg: 0x0060081C, Data: 0xA2566656
(II) NOUVEAU(0): 0xD012: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD012: Reg: 0x0060281C, Mask: 0xFFCFFFCC, Data: 0x00200022
(II) NOUVEAU(0): Read: Reg: 0x0060281C, Data: 0x22222222
(II) NOUVEAU(0): Write: Reg: 0x0060281C, Data: 0x22222222
(II) NOUVEAU(0): 0xD01F: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD01F: Reg: 0x00001864, Mask: 0xFFFFFFF0, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x00001864, Data: 0x00000000
```

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```
(II) NOUVEAU(0):      Write: Reg: 0x00001864, Data: 0x00000000
(II) NOUVEAU(0): 0xD02C: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00680404, Data: 0x00000000
(II) NOUVEAU(0): 0xD035: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x00100090, Data: 0xEFFFCFE0
(II) NOUVEAU(0): 0xD03E: [ (0x7A) - INIT_ZM_REG ]
(II) NOUVEAU(0):      Write: Reg: 0x0010022C, Data: 0x00000000
(II) NOUVEAU(0): 0xD047: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD047: Reg: 0x0068052C, Mask: 0xFFFFFEFF, Data: 0x00000001
(II) NOUVEAU(0):      Read:  Reg: 0x0068052C, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x0068052C, Data: 0x00000001
(II) NOUVEAU(0): 0xD054: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD054: Reg: 0x0068252C, Mask: 0xFFFFFFFF, Data: 0x00000101
(II) NOUVEAU(0):      Read:  Reg: 0x0068252C, Data: 0x00000101
(II) NOUVEAU(0):      Write: Reg: 0x0068252C, Data: 0x00000101
(II) NOUVEAU(0): 0xD061: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD061: Reg: 0x00680528, Mask: 0xFFFFFFFF, Data: 0x40000000
(II) NOUVEAU(0):      Read:  Reg: 0x00680528, Data: 0x40000000
(II) NOUVEAU(0):      Write: Reg: 0x00680528, Data: 0x40000000
(II) NOUVEAU(0): 0xD06E: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD06E: Reg: 0x00001584, Mask: 0xFFF00000, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0): 0xD07B: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD07B: Reg: 0x001002C0, Mask: 0xFFFFFEFF, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0):      Write: Reg: 0x001002C0, Data: 0x00000030
(II) NOUVEAU(0): 0xD088: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD088: Reg: 0x001002C0, Mask: 0xFFFFFFFF, Data: 0x00000100
(II) NOUVEAU(0):      Read:  Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0):      Write: Reg: 0x001002C0, Data: 0x00000130
(II) NOUVEAU(0): 0xD095: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD095: Reg: 0x001002E0, Mask: 0xFFFFFFFF, Data: 0x00000001
(II) NOUVEAU(0):      Read:  Reg: 0x001002E0, Data: 0x00000001
(II) NOUVEAU(0):      Write: Reg: 0x001002E0, Data: 0x00000001
(II) NOUVEAU(0): 0xD0A2: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD0A2: Reg: 0x006808B0, Mask: 0x00000000, Data: 0x0001003A
```


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```
(II) NOUVEAU(0):      Read:  Reg: 0x006808B0, Data: 0x0000003A
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x0001003A
(II) NOUVEAU(0): 0xD0AF: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD0AF: Reg: 0x006808B4, Mask: 0xFFFFFFFF, Data: 0x00000080
(II) NOUVEAU(0):      Read:  Reg: 0x006808B4, Data: 0x00000080
(II) NOUVEAU(0):      Write: Reg: 0x006808B4, Data: 0x00000080
(II) NOUVEAU(0): 0xD0BC: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD0BC: Reg: 0x006808B0, Mask: 0x00000000, Data: 0x0000003A
(II) NOUVEAU(0):      Read:  Reg: 0x006808B0, Data: 0x0000003A
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x0000003A
(II) NOUVEAU(0): 0xD0C9: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0): 0xD6FD: Parsing init table 3
(II) NOUVEAU(0): 0xD6FD: ----- EXECUTING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xD6FD: [ (0x34) - INIT_IO_RESTRICT_PLL ]
(II) NOUVEAU(0): 0xD6FD: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, IO Flag Condition: 0xF
0x00680500
(II) NOUVEAU(0):      Indexed IO read:  Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD6FD: Reg: 0x00680500, Config: 0x00, Freq: 200000kHz
(II) NOUVEAU(0):      Read:  Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Read:  Reg: 0x00680504, Data: 0x00004603
(II) NOUVEAU(0):      Read:  Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000010
(II) NOUVEAU(0):      Write: Reg: 0x00680504, Data: 0x00001C01
(II) NOUVEAU(0):      Read:  Reg: 0x00680504, Data: 0x00004603
(II) NOUVEAU(0):      Write: Reg: 0x00680504, Data: 0x00011C01
(II) NOUVEAU(0):      Write: Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0): 0xD729: [ (0x34) - INIT_IO_RESTRICT_PLL ]
(II) NOUVEAU(0): 0xD729: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, IO Flag Condition: 0x0
(II) NOUVEAU(0):      Indexed IO read:  Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD729: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, FlagArray: 0xCE18, FAM
(II) NOUVEAU(0):      Indexed IO read:  Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD729: Checking if 0x00 equals 0x01
(II) NOUVEAU(0): 0xD729: CONDITION IS NOT FULFILLED. FREQ UNCHANGED
(II) NOUVEAU(0): 0xD729: Reg: 0x00680504, Config: 0x00, Freq: 100000kHz
(II) NOUVEAU(0):      Read:  Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0):      Read:  Reg: 0x00680508, Data: 0x0004DB07
(II) NOUVEAU(0):      Read:  Reg: 0x00001584, Data: 0x00000000
```

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```
(II) NOUVEAU(0): Write: Reg: 0x00001584, Data: 0x00000100
(II) NOUVEAU(0): Write: Reg: 0x00680508, Data: 0x0004FB09
(II) NOUVEAU(0): Read: Reg: 0x00680508, Data: 0x0004DB07
(II) NOUVEAU(0): Write: Reg: 0x00680508, Data: 0x0002FB09
(II) NOUVEAU(0): Write: Reg: 0x00001584, Data: 0x00000000
(II) NOUVEAU(0): 0xD755: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0): 0xD0CA: Parsing init table 4
(II) NOUVEAU(0): 0xD0CA: ----- EXECUTING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xD0CA: [ (0x39) - INIT_IO_FLAG_CONDITION ]
(II) NOUVEAU(0): 0xD0CA: Port: 0x03D4, Index: 0x3C, Mask: 0xF0, Shift: 0x04, FlagArray: 0xCE18, FAM
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x3C, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): 0xD0CA: Checking if 0x00 equals 0x01
(II) NOUVEAU(0): 0xD0CA: CONDITION IS NOT FULFILLED
(II) NOUVEAU(0): 0xD0CA: ----- SKIPPING FOLLOWING COMMANDS -----
(II) NOUVEAU(0): 0xD0CC: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD0D9: [ (0x74) - INIT_TIME ]
(II) NOUVEAU(0): 0xD0DC: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD0E9: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD0F6: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xD103: [ (0x78) - INIT_INDEX_IO ]
(II) NOUVEAU(0): 0xD109: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0): Read: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0): Found flat panel mode table revision 1.0
(II) NOUVEAU(0): Found flat panel mode in BIOS tables:
(II) NOUVEAU(0): Modeline "1024x768"x0.0 65.00 1024 1048 1184 1344 768 771 777 806 -hsync -vsync
(II) NOUVEAU(0): Read: Reg: 0x00101000, Data: 0xA0A0E5C7
(II) NOUVEAU(0): Found LVDS manufacturer table revision 0.10
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x1F, Head: 0x00, Data: 0x99
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x11, Head: 0x00, Data: 0x8E
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x11, Head: 0x00, Data: 0x8E
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x03
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x1F, Head: 0x01, Data: 0x99
(II) NOUVEAU(0): Indexed IO read: Port: 0x03D4, Index: 0x11, Head: 0x01, Data: 0x8E
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x11, Head: 0x01, Data: 0x8E
(II) NOUVEAU(0): Display Configuration Block version 2.0 found
Raw DCB entry 0: 01000100 000088b8
```

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```
Raw DCB entry 1: 01000102 00000000
Raw DCB entry 2: 02010210 000088b8
Raw DCB entry 3: 04010212 00000000
(II) NOUVEAU(0): Found 4 entries in DCB
(II) Loading sub module "i2c"
(II) LoadModule: "i2c"(II) Module "i2c" already built-in
(II) Loading sub module "ddc"
(II) LoadModule: "ddc"(II) Module "ddc" already built-in
Setting owner: 0x0
Setting owner: 0x3
(-- ) NOUVEAU(0): DCB entry 0: type: 0, i2c_index: 0, heads: 1, bus: 0, or: 1
(II) NOUVEAU(0): I2C bus "DVI-A-0" initialized.
(II) NOUVEAU(0): Output DVI-A-0 using monitor section Philips 190B
(-- ) NOUVEAU(0): Added output DVI-A-0
(-- ) NOUVEAU(0): DCB entry 1: type: 2, i2c_index: 0, heads: 1, bus: 0, or: 1
(II) NOUVEAU(0): Output DVI-D-0 has no monitor section
(-- ) NOUVEAU(0): Added output DVI-D-0
(-- ) NOUVEAU(0): DCB entry 2: type: 0, i2c_index: 1, heads: 2, bus: 1, or: 2
(II) NOUVEAU(0): I2C bus "DVI-A-1" initialized.
(II) NOUVEAU(0): Output DVI-A-1 has no monitor section
(-- ) NOUVEAU(0): Added output DVI-A-1
(-- ) NOUVEAU(0): DCB entry 3: type: 2, i2c_index: 1, heads: 2, bus: 1, or: 4
(II) NOUVEAU(0): Output DVI-D-1 has no monitor section
(-- ) NOUVEAU(0): Added output DVI-D-1
nv_analog_output_detect is called
(II) NOUVEAU(0): I2C device "DVI-A-0:ddc2" registered at address 0xA0.
(II) NOUVEAU(0): I2C device "DVI-A-0:ddc2" removed.
(II) NOUVEAU(0): I2C device "DVI-A-0:ddc2" registered at address 0xA0.
(II) NOUVEAU(0): I2C device "DVI-A-0:ddc2" removed.
nv_tmds_output_detect is called
(II) NOUVEAU(0): I2C device "DVI-A-0:ddc2" registered at address 0xA0.
(II) NOUVEAU(0): I2C device "DVI-A-0:ddc2" removed.
nv_analog_output_detect is called
(II) NOUVEAU(0): I2C device "DVI-A-1:ddc2" registered at address 0xA0.
nv_output_get_ddc_modes is called
nv_output_get_modes is called
(II) NOUVEAU(0): EDID vendor "PHL", prod id 2096
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nv tmds_output_detect is called

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(II) NOUVEAU(0): Output DVI-A-0 disconnected
(II) NOUVEAU(0): Output DVI-D-0 disconnected
(II) NOUVEAU(0): Output DVI-A-1 connected
(II) NOUVEAU(0): Output DVI-D-1 disconnected
(II) NOUVEAU(0): Output DVI-A-1 using initial mode 1280x1024
(-- ) NOUVEAU(0): VideoRAM: 65536 kBytes
(==) NOUVEAU(0): Using gamma correction (1.0, 1.0, 1.0)
(-- ) NOUVEAU(0): Virtual size is 1280x1280 (pitch 1280)
(**) NOUVEAU(0): Driver mode "1280x1024": 108.0 MHz (scaled from 0.0 MHz), 64.0 kHz, 60.0 Hz
(II) NOUVEAU(0): Modeline "1280x1024"x60.0 108.00 1280 1328 1440 1688 1024 1025 1028 1066 +hsync
(**) NOUVEAU(0): Driver mode "1280x1024": 135.0 MHz (scaled from 0.0 MHz), 80.0 kHz, 75.0 Hz
(II) NOUVEAU(0): Modeline "1280x1024"x75.0 135.00 1280 1296 1440 1688 1024 1025 1028 1066 +hsync
(**) NOUVEAU(0): Driver mode "1280x1024": 109.0 MHz (scaled from 0.0 MHz), 63.7 kHz, 59.9 Hz
(II) NOUVEAU(0): Modeline "1280x1024"x59.9 109.00 1280 1368 1496 1712 1024 1027 1034 1063 -hsync
(**) NOUVEAU(0): Driver mode "1152x864": 108.0 MHz (scaled from 0.0 MHz), 67.5 kHz, 75.0 Hz
(II) NOUVEAU(0): Modeline "1152x864"x75.0 108.00 1152 1216 1344 1600 864 865 868 900 +hsync +vsync
(**) NOUVEAU(0): Driver mode "1024x768": 78.8 MHz (scaled from 0.0 MHz), 60.1 kHz, 75.1 Hz
(II) NOUVEAU(0): Modeline "1024x768"x75.1 78.80 1024 1040 1136 1312 768 769 772 800 +hsync +vsync
(**) NOUVEAU(0): Driver mode "1024x768": 75.0 MHz (scaled from 0.0 MHz), 56.5 kHz, 70.1 Hz
(II) NOUVEAU(0): Modeline "1024x768"x70.1 75.00 1024 1048 1184 1328 768 771 777 806 -hsync -vsync
(**) NOUVEAU(0): Driver mode "1024x768": 65.0 MHz (scaled from 0.0 MHz), 48.4 kHz, 60.0 Hz
(II) NOUVEAU(0): Modeline "1024x768"x60.0 65.00 1024 1048 1184 1344 768 771 777 806 -hsync -vsync
(**) NOUVEAU(0): Driver mode "832x624": 57.3 MHz (scaled from 0.0 MHz), 49.7 kHz, 74.6 Hz
(II) NOUVEAU(0): Modeline "832x624"x74.6 57.28 832 864 928 1152 624 625 628 667 -hsync -vsync (4
(**) NOUVEAU(0): Driver mode "800x600": 50.0 MHz (scaled from 0.0 MHz), 48.1 kHz, 72.2 Hz
(II) NOUVEAU(0): Modeline "800x600"x72.2 50.00 800 856 976 1040 600 637 643 666 +hsync +vsync (4
(**) NOUVEAU(0): Driver mode "800x600": 49.5 MHz (scaled from 0.0 MHz), 46.9 kHz, 75.0 Hz
(II) NOUVEAU(0): Modeline "800x600"x75.0 49.50 800 816 896 1056 600 601 604 625 +hsync +vsync (4
(**) NOUVEAU(0): Driver mode "800x600": 40.0 MHz (scaled from 0.0 MHz), 37.9 kHz, 60.3 Hz
(II) NOUVEAU(0): Modeline "800x600"x60.3 40.00 800 840 968 1056 600 601 605 628 +hsync +vsync (3
(**) NOUVEAU(0): Driver mode "800x600": 36.0 MHz (scaled from 0.0 MHz), 35.2 kHz, 56.2 Hz
(II) NOUVEAU(0): Modeline "800x600"x56.2 36.00 800 824 896 1024 600 601 603 625 +hsync +vsync (3
(**) NOUVEAU(0): Driver mode "640x480": 31.5 MHz (scaled from 0.0 MHz), 37.5 kHz, 75.0 Hz
(II) NOUVEAU(0): Modeline "640x480"x75.0 31.50 640 656 720 840 480 481 484 500 -hsync -vsync (3
(**) NOUVEAU(0): Driver mode "640x480": 31.5 MHz (scaled from 0.0 MHz), 37.9 kHz, 72.8 Hz
(II) NOUVEAU(0): Modeline "640x480"x72.8 31.50 640 664 704 832 480 489 491 520 -hsync -vsync (3
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(**) NOUVEAU(0): Driver mode "640x480": 30.2 MHz (scaled from 0.0 MHz), 35.0 kHz, 66.7 Hz
(II) NOUVEAU(0): Modeline "640x480"x66.7 30.24 640 704 768 864 480 483 486 525 -hsync -vsync (3
(**) NOUVEAU(0): Driver mode "640x480": 25.2 MHz (scaled from 0.0 MHz), 31.5 kHz, 60.0 Hz
(II) NOUVEAU(0): Modeline "640x480"x60.0 25.20 640 656 752 800 480 490 492 525 -hsync -vsync (3
(**) NOUVEAU(0): Driver mode "720x400": 28.3 MHz (scaled from 0.0 MHz), 31.5 kHz, 70.1 Hz
(II) NOUVEAU(0): Modeline "720x400"x70.1 28.32 720 738 846 900 400 412 414 449 -hsync +vsync (3
(++ NOUVEAU(0): DPI set to (100, 100)
(II) Loading sub module "fb"
(II) LoadModule: "fb"
(II) Loading /usr/lib/xorg/modules//libfb.so
(II) Module fb: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 1.0.0
        ABI class: X.Org ANSI C Emulation, version 0.3
(II) Loading sub module "exa"
(II) LoadModule: "exa"
(II) Loading /usr/lib/xorg/modules//libexa.so
(II) Module exa: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 2.2.0
        ABI class: X.Org Video Driver, version 2.0
(II) Loading sub module "ramdac"
(II) LoadModule: "ramdac"(II) Module "ramdac" already built-in
(-- Depth 24 pixmap format is 32 bpp
(II) do I need RAC? No, I don't.
(II) resource ranges after preInit:
    [0] 0 0 0xd0000000 - 0xd0000000 (0x000000) MX[B]
    [1] 0 0 0xe0000000 - 0xe0000000 (0x000000) MX[B]
    [2] 0 0 0xf0000000 - 0xf0000000 (0x000000) MX[B]
    [3] -1 0 0x00100000 - 0x3ffffff (0x3ff00000) MX[B]E(B)
    [4] -1 0 0x000f0000 - 0x000ffff (0x10000) MX[B]
    [5] -1 0 0x000c0000 - 0x000effff (0x30000) MX[B]
    [6] -1 0 0x00000000 - 0x0009ffff (0xa0000) MX[B]
    [7] -1 0 0xe7800000 - 0xe77ffff (0x0) MX[B]0
    [8] -1 0 0xdf7e0000 - 0xdf7ffff (0x20000) MX[B](B)
    [9] -1 0 0xdf800000 - 0xdf87ffff (0x80000) MX[B](B)
    [10] -1 0 0xe0000000 - 0xe3ffffff (0x4000000) MX[B](B)
    [11] -1 0 0xf0000000 - 0xf0000000 (0x0) MX[B](B)
    [12] 0 0 0x000a0000 - 0x000affff (0x10000) MS[B](OprD)
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
[13] 0 0      0x000b0000 - 0x000b7fff (0x8000) MS[B](OprD)
[14] 0 0      0x000b8000 - 0x000bffff (0x8000) MS[B](OprD)
[15] -1 0     0x0000ffff - 0x0000ffff (0x1) IX[B]
[16] -1 0     0x00000000 - 0x000000ff (0x100) IX[B]
[17] -1 0     0x0000a400 - 0x0000a43f (0x40) IX[B]
[18] -1 0     0x0000a800 - 0x0000a81f (0x20) IX[B]
[19] -1 0     0x0000b000 - 0x0000b00f (0x10) IX[B]
[20] -1 0     0x0000b400 - 0x0000b403 (0x4) IX[B]
[21] -1 0     0x0000b800 - 0x0000b803 (0x4) IX[B]
[22] -1 0     0x0000d000 - 0x0000d00f (0x10) IX[B]
[23] -1 0     0x0000d400 - 0x0000d40f (0x10) IX[B]
[24] -1 0     0x0000d800 - 0x0000d83f (0x40) IX[B]
[25] 0 0      0x000003b0 - 0x000003bb (0xc) IS[B](OprU)
[26] 0 0      0x000003c0 - 0x000003df (0x20) IS[B](OprU)
```

```
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 7, (OK)
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 7, (OK)
drmOpenByBusid: Searching for BusID pci:0000:01:00.0
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 7, (OK)
drmOpenByBusid: drmOpenMinor returns 7
drmOpenByBusid: drmGetBusid reports pci:0000:01:00.0
(II) [drm] DRM interface version 1.3
(II) [drm] DRM open master succeeded.
(II) NOUVEAU(0): [drm] Using the DRM lock SAREA also for drawables.
(II) NOUVEAU(0): [drm] framebuffer handle = 0xe2000000
(II) NOUVEAU(0): [drm] added 1 reserved context for kernel
(II) NOUVEAU(0): X context handle = 0x1
(II) NOUVEAU(0): [drm] installed DRM signal handler
(II) NOUVEAU(0): Allocated 32MiB VRAM for framebuffer + offscreen pixmaps
(II) NOUVEAU(0): AGPGART: 4MiB available
(II) NOUVEAU(0): GART: Allocated 3MiB as a scratch buffer
(II) NOUVEAU(0): Opened GPU channel 1
CRTC 0: Console mode: 640x144 depth: 4 bpp: 4 crtc_start: 0x0
CRTC 1: Console mode: 640x144 depth: 4 bpp: 4 crtc_start: 0x0
NVEnterVT is called
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
nv_crtc_save is called for CRTC 0
Setting owner: 0x0
nv_crtc_save is called for CRTC 1
Setting owner: 0x3
nv_output_save is called
nv_output_save is called
nv_output_save is called
nv_output_save is called
Setting owner: 0x0
Setting owner: 0x3
nv_analog_output_dpms is called with mode 3
nv_tmds_output_dpms is called with mode 3
nv_analog_output_dpms is called with mode 3
nv_tmds_output_dpms is called with mode 3
nv_crtc_dpms is called for CRTC 0 with mode 3
Setting owner: 0x0
nv_crtc_dpms is called for CRTC 1 with mode 3
Setting owner: 0x3
nv_crtc_lock is called for CRTC 1
nv_output_mode_fixup is called
nv_crtc_mode_fixup is called for CRTC 1
nv_output_prepare is called
nv_analog_output_dpms is called with mode 3
nv_crtc_prepare is called for CRTC 1
Setting owner: 0x3
nv_crtc_dpms is called for CRTC 1 with mode 3
Setting owner: 0x3
nv_crtc_mode_set is called for CRTC 1
(WW) NOUVEAU(0): Mode on CRTC 1
(II) NOUVEAU(0): Modeline "1280x1024"x60.0  108.00  1280 1328 1440 1688  1024 1025 1028 1066 +hsync
Setting owner: 0x3
Mode clock: 108000
Adjusted mode clock: 108000
horizDisplay: 0x9F
horizStart: 0xA5
horizEnd: 0xB3
horizTotal: 0xCE
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
horizBlankStart: 0x9F
horizBlankEnd: 0xD2
vertDisplay: 0x3FF
vertStart: 0x400
vertEnd: 0x403
vertTotal: 0x428
vertBlankStart: 0x3FF
vertBlankEnd: 0x429
crtc 1 1280 1280
(II) NOUVEAU(0):          Read:  Reg: 0x00101000, Data: 0xA0A0E5C7
vpll: n 181 m 6 log2p 2
There are 1 CRTC's enabled
output 1 debug_0 01101100
writing sel_clk 00000000
writing vpll2_a 0002B506
writing pllsel 20000D00
NVCrtcSetBase: x: 0 y: 0
nv_output_mode_set is called
0: crtc 1 output 1 twocrt 0 twomon 0
output reg: 0x0 0x101
nv_crtc_commit for CRTC 1
nv_crtc_dpms is called for CRTC 1 with mode 0
Setting owner: 0x3
nv_output_commit is called
nv_analog_output_dpms is called with mode 0
nv_analog_output_dpms is called with mode 3
nv_tmds_output_dpms is called with mode 3
nv_tmds_output_dpms is called with mode 3
nv_crtc_dpms is called for CRTC 0 with mode 3
Setting owner: 0x3
(II) EXA(0): Offscreen pixmap area of 27000832 bytes
(II) EXA(0): Driver registered support for the following operations:
(II)          Solid
(II)          Copy
(II)          Composite (RENDER acceleration)
(II)          UploadToScreen
(II)          DownloadFromScreen
```


File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(==) NOUVEAU(0): Backing store disabled
(==) NOUVEAU(0): Silken mouse enabled
(II) NOUVEAU(0): [DRI] installation complete
(**) Option "dpms"
(**) NOUVEAU(0): DPMS enabled
(II) NOUVEAU(0): RandR 1.2 enabled, ignore the following RandR disabled message.
(II) NOUVEAU(0): Xv: Composite is enabled, enabling overlay with smart blitter fallback
(II) NOUVEAU(0): Xv: Randr12 is enabled, using overlay with smart blitter fallback and automatic CR
(-- ) RandR disabled
(II) Initializing built-in extension MIT-SHM
(II) Initializing built-in extension XInputExtension
(II) Initializing built-in extension XTEST
(II) Initializing built-in extension XKEYBOARD
(II) Initializing built-in extension XC-APPGROUP
(II) Initializing built-in extension XAccessControlExtension
(II) Initializing built-in extension SECURITY
(II) Initializing built-in extension XINERAMA
(II) Initializing built-in extension XFIXES
(II) Initializing built-in extension XFree86-Bigfont
(II) Initializing built-in extension RENDER
(II) Initializing built-in extension RANDR
(II) Initializing built-in extension COMPOSITE
(II) Initializing built-in extension DAMAGE
(II) Initializing built-in extension XEVIE
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 8, (OK)
drmOpenByBusid: Searching for BusID pci:0000:01:00.0
drmOpenDevice: node name is /dev/dri/card0
drmOpenDevice: open result is 8, (OK)
drmOpenByBusid: drmOpenMinor returns 8
drmOpenByBusid: drmGetBusid reports pci:0000:01:00.0
(EE) AIGLX error: dlopen of /usr/lib/dri/nouveau_dri.so failed (/usr/lib/dri/nouveau_dri.so: cannot
file or directory)
(EE) AIGLX: reverting to software rendering
(II) Loading sub module "GLcore"
(II) LoadModule: "GLcore"
(II) Loading /usr/lib/xorg/modules/extensions//libGLcore.so
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) Module GLcore: vendor="X.Org Foundation"
        compiled for 1.4.0.90, module version = 1.0.0
        ABI class: X.Org Server Extension, version 0.3
(II) GLX: Initialized MESA-PROXY GL provider for screen 0
(II) NOUVEAU(0): Setting screen physical size to 325 x 260
nv_xf86crtc_resize is called with 1280x1024 resolution
(WW) <default pointer>: No Device specified, looking for one...
(II) <default pointer>: Setting Device option to "/dev/input/mice"
(--> <default pointer>: Device: "/dev/input/mice"
(==> <default pointer>: Protocol: "Auto"
(**) Option "CorePointer"
(**) <default pointer>: always reports core events
(==> <default pointer>: Emulate3Buttons, Emulate3Timeout: 50
(**) <default pointer>: ZAxisMapping: buttons 4 and 5
(**) <default pointer>: Buttons: 9
(**) <default pointer>: Sensitivity: 1
(**) Option "CoreKeyboard"
(**) <default keyboard>: always reports core events
(**) Option "Protocol" "standard"
(**) <default keyboard>: Protocol: standard
(**) Option "AutoRepeat" "500 30"
(**) Option "XkbRules" "xorg"
(**) <default keyboard>: XkbRules: "xorg"
(**) Option "XkbModel" "pc105"
(**) <default keyboard>: XkbModel: "pc105"
(**) Option "XkbLayout" "us"
(**) <default keyboard>: XkbLayout: "us"
(**) Option "CustomKeycodes" "off"
(**) <default keyboard>: CustomKeycodes disabled
(II) evaluating device (<default keyboard>)
(II) XINPUT: Adding extended input device "<default keyboard>" (type: KEYBOARD)
(II) evaluating device (<default pointer>)
(II) XINPUT: Adding extended input device "<default pointer>" (type: MOUSE)
(--> <default pointer>: PnP-detected protocol: "ExplorerPS/2"
(II) <default pointer>: ps2EnableDataReporting: succeeded
Setting owner: 0x3
(II) NOUVEAU(0): Fifo dump (lockup 0x0c85,0x0e0a):
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0c71] 0x00084400
(II) NOUVEAU(0): [0x0c72] 0x0006000b
(II) NOUVEAU(0): [0x0c73] 0x00010004
(II) NOUVEAU(0): [0x0c74] 0x00084400
(II) NOUVEAU(0): [0x0c75] 0x000b000b
(II) NOUVEAU(0): [0x0c76] 0x00010004
(II) NOUVEAU(0): [0x0c77] 0x00084400
(II) NOUVEAU(0): [0x0c78] 0x0006000f
(II) NOUVEAU(0): [0x0c79] 0x00060001
(II) NOUVEAU(0): [0x0c7a] 0x0004c2fc
(II) NOUVEAU(0): [0x0c7b] 0x00000003
(II) NOUVEAU(0): [0x0c7c] 0x00102300
(II) NOUVEAU(0): [0x0c7d] 0x0000000a
(II) NOUVEAU(0): [0x0c7e] 0x14000080
(II) NOUVEAU(0): [0x0c7f] 0x00064d00
(II) NOUVEAU(0): [0x0c80] 0x00009000
(II) NOUVEAU(0): [0x0c81] 0x000cc300
(II) NOUVEAU(0): [0x0c82] 0x00000000
(II) NOUVEAU(0): [0x0c83] 0x03ea04b5
(II) NOUVEAU(0): [0x0c84] 0x00140019
(II) NOUVEAU(0): [0x0c85] 0x0004c2fc
(II) NOUVEAU(0): [0x0c86] 0x00000003
(II) NOUVEAU(0): [0x0c87] 0x00102300
(II) NOUVEAU(0): [0x0c88] 0x0000000a
(II) NOUVEAU(0): [0x0c89] 0x14001400
(II) NOUVEAU(0): [0x0c8a] 0x00009000
(II) NOUVEAU(0): [0x0c8b] 0x00009000
(II) NOUVEAU(0): [0x0c8c] 0x000cc300
(II) NOUVEAU(0): [0x0c8d] 0x01f0013b
(II) NOUVEAU(0): [0x0c8e] 0x01f00138
(II) NOUVEAU(0): [0x0c8f] 0x0001012c
(II) NOUVEAU(0): [0x0c90] 0x000cc300
(II) NOUVEAU(0): [0x0c91] 0x01f10139
(II) NOUVEAU(0): [0x0c92] 0x01f10136
(II) NOUVEAU(0): [0x0c93] 0x00010130
(II) NOUVEAU(0): [0x0c94] 0x000cc300
(II) NOUVEAU(0): [0x0c95] 0x01f20138

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0c96] 0x01f20135
(II) NOUVEAU(0): [0x0c97] 0x00010132
(II) NOUVEAU(0): [0x0c98] 0x000cc300
(II) NOUVEAU(0): [0x0c99] 0x01f30137
(II) NOUVEAU(0): [0x0c9a] 0x01f30134
(II) NOUVEAU(0): [0x0c9b] 0x00020134
(II) NOUVEAU(0): [0x0c9c] 0x000cc300
(II) NOUVEAU(0): [0x0c9d] 0x01f50136
(II) NOUVEAU(0): [0x0c9e] 0x01f50133
(II) NOUVEAU(0): [0x0c9f] 0x013f0136
(II) NOUVEAU(0): [0x0ca0] 0x000cc300
(II) NOUVEAU(0): [0x0ca1] 0x03340137
(II) NOUVEAU(0): [0x0ca2] 0x03340134
(II) NOUVEAU(0): [0x0ca3] 0x00020134
(II) NOUVEAU(0): [0x0ca4] 0x000cc300
(II) NOUVEAU(0): [0x0ca5] 0x03360138
(II) NOUVEAU(0): [0x0ca6] 0x03360135
(II) NOUVEAU(0): [0x0ca7] 0x00010132
(II) NOUVEAU(0): [0x0ca8] 0x000cc300
(II) NOUVEAU(0): [0x0ca9] 0x03370139
(II) NOUVEAU(0): [0x0caa] 0x03370136
(II) NOUVEAU(0): [0x0cab] 0x00010130
(II) NOUVEAU(0): [0x0cac] 0x000cc300
(II) NOUVEAU(0): [0x0cad] 0x0338013b
(II) NOUVEAU(0): [0x0cae] 0x03380138
(II) NOUVEAU(0): [0x0caf] 0x0001012c
(II) NOUVEAU(0): [0x0cb0] 0x000442fc
(II) NOUVEAU(0): [0x0cb1] 0x00000003
(II) NOUVEAU(0): [0x0cb2] 0x00102300
(II) NOUVEAU(0): [0x0cb3] 0x0000000b
(II) NOUVEAU(0): [0x0cb4] 0x14001400
(II) NOUVEAU(0): [0x0cb5] 0x00009000
(II) NOUVEAU(0): [0x0cb6] 0x00009000
(II) NOUVEAU(0): [0x0cb7] 0x00044300
(II) NOUVEAU(0): [0x0cb8] 0x00000003
(II) NOUVEAU(0): [0x0cb9] 0x000443fc
(II) NOUVEAU(0): [0x0cba] 0x00efeb7

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0cbb] 0x00084400
(II) NOUVEAU(0): [0x0cbc] 0x026801f1
(II) NOUVEAU(0): [0x0cbd] 0x00010001
(II) NOUVEAU(0): [0x0cbe] 0x00084400
(II) NOUVEAU(0): [0x0cbf] 0x026701f2
(II) NOUVEAU(0): [0x0cc0] 0x00030001
(II) NOUVEAU(0): [0x0cc1] 0x00084400
(II) NOUVEAU(0): [0x0cc2] 0x026801f3
(II) NOUVEAU(0): [0x0cc3] 0x00030002
(II) NOUVEAU(0): [0x0cc4] 0x00084400
(II) NOUVEAU(0): [0x0cc5] 0x026901f5
(II) NOUVEAU(0): [0x0cc6] 0x00030013
(II) NOUVEAU(0): [0x0cc7] 0x00084400
(II) NOUVEAU(0): [0x0cc8] 0x02690208
(II) NOUVEAU(0): [0x0cc9] 0x0001012c
(II) NOUVEAU(0): [0x0cca] 0x00084400
(II) NOUVEAU(0): [0x0ccb] 0x02680334
(II) NOUVEAU(0): [0x0ccc] 0x00020002
(II) NOUVEAU(0): [0x0ccd] 0x00084400
(II) NOUVEAU(0): [0x0cce] 0x02670336
(II) NOUVEAU(0): [0x0ccf] 0x00030001
(II) NOUVEAU(0): [0x0cd0] 0x00084400
(II) NOUVEAU(0): [0x0cd1] 0x02660337
(II) NOUVEAU(0): [0x0cd2] 0x00030001
(II) NOUVEAU(0): [0x0cd3] 0x00084400
(II) NOUVEAU(0): [0x0cd4] 0x02650338
(II) NOUVEAU(0): [0x0cd5] 0x00020001
(II) NOUVEAU(0): [0x0cd6] 0x000442fc
(II) NOUVEAU(0): [0x0cd7] 0x00000003
(II) NOUVEAU(0): [0x0cd8] 0x00102300
(II) NOUVEAU(0): [0x0cd9] 0x0000000b
(II) NOUVEAU(0): [0x0cda] 0x14001400
(II) NOUVEAU(0): [0x0cdb] 0x00009000
(II) NOUVEAU(0): [0x0cdc] 0x00009000
(II) NOUVEAU(0): [0x0cdd] 0x00044300
(II) NOUVEAU(0): [0x0cde] 0x00000003
(II) NOUVEAU(0): [0x0cdf] 0x000443fc

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0): [0x0ce0] 0x00ffffff
(II) NOUVEAU(0): [0x0ce1] 0x00084400
(II) NOUVEAU(0): [0x0ce2] 0x026a0221
(II) NOUVEAU(0): [0x0ce3] 0x00020113
(II) NOUVEAU(0): [0x0ce4] 0x00084400
(II) NOUVEAU(0): [0x0ce5] 0x026a0334
(II) NOUVEAU(0): [0x0ce6] 0x00010002
(II) NOUVEAU(0): [0x0ce7] 0x000442fc
(II) NOUVEAU(0): [0x0ce8] 0x00000003
(II) NOUVEAU(0): [0x0ce9] 0x00102300
(II) NOUVEAU(0): [0x0cea] 0x0000000b
(II) NOUVEAU(0): [0x0ceb] 0x14001400
(II) NOUVEAU(0): [0x0cec] 0x00009000
(II) NOUVEAU(0): [0x0ced] 0x00009000
(II) NOUVEAU(0): [0x0cee] 0x00044300
(II) NOUVEAU(0): [0x0cef] 0x00000003
(II) NOUVEAU(0): [0x0cf0] 0x000443fc
(II) NOUVEAU(0): [0x0cf1] 0x00efeb7
(II) NOUVEAU(0): [0x0cf2] 0x00084400
(II) NOUVEAU(0): [0x0cf3] 0x026a0208
(II) NOUVEAU(0): [0x0cf4] 0x00020019
(II) NOUVEAU(0): [0x0cf5] 0x000442fc
(II) NOUVEAU(0): [0x0cf6] 0x00000003
(II) NOUVEAU(0): [0x0cf7] 0x00102300
(II) NOUVEAU(0): [0x0cf8] 0x0000000b
(II) NOUVEAU(0): [0x0cf9] 0x14001400
(II) NOUVEAU(0): [0x0cfa] 0x00009000
(II) NOUVEAU(0): [0x0cfb] 0x00009000
(II) NOUVEAU(0): [0x0cfc] 0x00044300
(II) NOUVEAU(0): [0x0cfd] 0x00000003
(II) NOUVEAU(0): [0x0cfe] 0x000443fc
(II) NOUVEAU(0): [0x0cff] 0x002c160a
(II) NOUVEAU(0): [0x0d00] 0x00084400
(II) NOUVEAU(0): [0x0d01] 0x026401f0
(II) NOUVEAU(0): [0x0d02] 0x00030001
(II) NOUVEAU(0): [0x0d03] 0x00084400
(II) NOUVEAU(0): [0x0d04] 0x026601f1
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0d05] 0x00020001
(II) NOUVEAU(0): [0x0d06] 0x00084400
(II) NOUVEAU(0): [0x0d07] 0x02640338
(II) NOUVEAU(0): [0x0d08] 0x00010001
(II) NOUVEAU(0): [0x0d09] 0x000442fc
(II) NOUVEAU(0): [0x0d0a] 0x00000003
(II) NOUVEAU(0): [0x0d0b] 0x00102300
(II) NOUVEAU(0): [0x0d0c] 0x0000000b
(II) NOUVEAU(0): [0x0d0d] 0x00400040
(II) NOUVEAU(0): [0x0d0e] 0x0064dd00
(II) NOUVEAU(0): [0x0d0f] 0x0064dd00
(II) NOUVEAU(0): [0x0d10] 0x00044300
(II) NOUVEAU(0): [0x0d11] 0x00000003
(II) NOUVEAU(0): [0x0d12] 0x000443fc
(II) NOUVEAU(0): [0x0d13] 0x00efeb7
(II) NOUVEAU(0): [0x0d14] 0x00084400
(II) NOUVEAU(0): [0x0d15] 0x00000000
(II) NOUVEAU(0): [0x0d16] 0x00020019
(II) NOUVEAU(0): [0x0d17] 0x00102300
(II) NOUVEAU(0): [0x0d18] 0x0000000a
(II) NOUVEAU(0): [0x0d19] 0x00400040
(II) NOUVEAU(0): [0x0d1a] 0x0064e400
(II) NOUVEAU(0): [0x0d1b] 0x0064e400
(II) NOUVEAU(0): [0x0d1c] 0x00088300
(II) NOUVEAU(0): [0x0d1d] 0x00000000
(II) NOUVEAU(0): [0x0d1e] 0x7fff7fff
(II) NOUVEAU(0): [0x0d1f] 0x0008a2fc
(II) NOUVEAU(0): [0x0d20] 0x00000003
(II) NOUVEAU(0): [0x0d21] 0x00000004
(II) NOUVEAU(0): [0x0d22] 0x000ca304
(II) NOUVEAU(0): [0x0d23] 0x00000000
(II) NOUVEAU(0): [0x0d24] 0x00190002
(II) NOUVEAU(0): [0x0d25] 0x00190002
(II) NOUVEAU(0): [0x0d26] 0x0008a400
(II) NOUVEAU(0): [0x0d27] 0xffeaae6
(II) NOUVEAU(0): [0x0d28] 0xffeaae6
(II) NOUVEAU(0): [0x0d29] 0x0008a400

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0): [0x0d2a] 0xffeaae6
(II) NOUVEAU(0): [0x0d2b] 0xffeaae6
(II) NOUVEAU(0): [0x0d2c] 0x0008a400
(II) NOUVEAU(0): [0x0d2d] 0xffeaae5
(II) NOUVEAU(0): [0x0d2e] 0xffeaae5
(II) NOUVEAU(0): [0x0d2f] 0x0008a400
(II) NOUVEAU(0): [0x0d30] 0xffede9e5
(II) NOUVEAU(0): [0x0d31] 0xffede9e5
(II) NOUVEAU(0): [0x0d32] 0x0008a400
(II) NOUVEAU(0): [0x0d33] 0xffede9e4
(II) NOUVEAU(0): [0x0d34] 0xffede9e4
(II) NOUVEAU(0): [0x0d35] 0x0008a400
(II) NOUVEAU(0): [0x0d36] 0xffede8e4
(II) NOUVEAU(0): [0x0d37] 0xffede8e4
(II) NOUVEAU(0): [0x0d38] 0x0008a400
(II) NOUVEAU(0): [0x0d39] 0xffece8e3
(II) NOUVEAU(0): [0x0d3a] 0xffece8e3
(II) NOUVEAU(0): [0x0d3b] 0x0008a400
(II) NOUVEAU(0): [0x0d3c] 0xffece7e3
(II) NOUVEAU(0): [0x0d3d] 0xffece7e3
(II) NOUVEAU(0): [0x0d3e] 0x0008a400
(II) NOUVEAU(0): [0x0d3f] 0xffebe7e2
(II) NOUVEAU(0): [0x0d40] 0xffebe7e2
(II) NOUVEAU(0): [0x0d41] 0x0008a400
(II) NOUVEAU(0): [0x0d42] 0xffebe6e2
(II) NOUVEAU(0): [0x0d43] 0xffebe6e2
(II) NOUVEAU(0): [0x0d44] 0x0008a400
(II) NOUVEAU(0): [0x0d45] 0xffebe6e1
(II) NOUVEAU(0): [0x0d46] 0xffebe6e1
(II) NOUVEAU(0): [0x0d47] 0x0008a400
(II) NOUVEAU(0): [0x0d48] 0xffeae5e1
(II) NOUVEAU(0): [0x0d49] 0xffeae5e1
(II) NOUVEAU(0): [0x0d4a] 0x0008a400
(II) NOUVEAU(0): [0x0d4b] 0xffeae5e0
(II) NOUVEAU(0): [0x0d4c] 0xffeae5e0
(II) NOUVEAU(0): [0x0d4d] 0x0008a400
(II) NOUVEAU(0): [0x0d4e] 0xffeae5df
```


File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0): [0x0d4f] 0xffeae5df
(II) NOUVEAU(0): [0x0d50] 0x0008a400
(II) NOUVEAU(0): [0x0d51] 0xffe9e4df
(II) NOUVEAU(0): [0x0d52] 0xffe9e4df
(II) NOUVEAU(0): [0x0d53] 0x0008a400
(II) NOUVEAU(0): [0x0d54] 0xffe9e4de
(II) NOUVEAU(0): [0x0d55] 0xffe9e4de
(II) NOUVEAU(0): [0x0d56] 0x0008a400
(II) NOUVEAU(0): [0x0d57] 0xffe9e3de
(II) NOUVEAU(0): [0x0d58] 0xffe9e3de
(II) NOUVEAU(0): [0x0d59] 0x0008a400
(II) NOUVEAU(0): [0x0d5a] 0xffe8e3dd
(II) NOUVEAU(0): [0x0d5b] 0xffe8e3dd
(II) NOUVEAU(0): [0x0d5c] 0x0008a400
(II) NOUVEAU(0): [0x0d5d] 0xffe8e2dd
(II) NOUVEAU(0): [0x0d5e] 0xffe8e2dd
(II) NOUVEAU(0): [0x0d5f] 0x0008a400
(II) NOUVEAU(0): [0x0d60] 0xffe8e2dc
(II) NOUVEAU(0): [0x0d61] 0xffe8e2dc
(II) NOUVEAU(0): [0x0d62] 0x0008a400
(II) NOUVEAU(0): [0x0d63] 0xffe7e2dc
(II) NOUVEAU(0): [0x0d64] 0xffe7e2dc
(II) NOUVEAU(0): [0x0d65] 0x0008a400
(II) NOUVEAU(0): [0x0d66] 0xffe7e1db
(II) NOUVEAU(0): [0x0d67] 0xffe7e1db
(II) NOUVEAU(0): [0x0d68] 0x0008a400
(II) NOUVEAU(0): [0x0d69] 0xffe6e1db
(II) NOUVEAU(0): [0x0d6a] 0xffe6e1db
(II) NOUVEAU(0): [0x0d6b] 0x0008a400
(II) NOUVEAU(0): [0x0d6c] 0xffe6e0da
(II) NOUVEAU(0): [0x0d6d] 0xffe6e0da
(II) NOUVEAU(0): [0x0d6e] 0x0008a400
(II) NOUVEAU(0): [0x0d6f] 0xffe6e0da
(II) NOUVEAU(0): [0x0d70] 0xffe6e0da
(II) NOUVEAU(0): [0x0d71] 0x00100208
(II) NOUVEAU(0): [0x0d72] 0x00000108
(II) NOUVEAU(0): [0x0d73] 0x00400040
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0d74] 0x0064dd00
(II) NOUVEAU(0): [0x0d75] 0x00000000
(II) NOUVEAU(0): [0x0d76] 0x00080200
(II) NOUVEAU(0): [0x0d77] 0x08000000
(II) NOUVEAU(0): [0x0d78] 0x08000000
(II) NOUVEAU(0): [0x0d79] 0x000402b4
(II) NOUVEAU(0): [0x0d7a] 0x00000000
(II) NOUVEAU(0): [0x0d7b] 0x000402c0
(II) NOUVEAU(0): [0x0d7c] 0x0fff0800
(II) NOUVEAU(0): [0x0d7d] 0x000402e0
(II) NOUVEAU(0): [0x0d7e] 0x0fff0800
(II) NOUVEAU(0): [0x0d7f] 0x00400500
(II) NOUVEAU(0): [0x0d80] 0x3f800000
(II) NOUVEAU(0): [0x0d81] 0x00000000
(II) NOUVEAU(0): [0x0d82] 0x00000000
(II) NOUVEAU(0): [0x0d83] 0x00000000
(II) NOUVEAU(0): [0x0d84] 0x00000000
(II) NOUVEAU(0): [0x0d85] 0x3f800000
(II) NOUVEAU(0): [0x0d86] 0x00000000
(II) NOUVEAU(0): [0x0d87] 0x00000000
(II) NOUVEAU(0): [0x0d88] 0x00000000
(II) NOUVEAU(0): [0x0d89] 0x00000000
(II) NOUVEAU(0): [0x0d8a] 0x3f800000
(II) NOUVEAU(0): [0x0d8b] 0x00000000
(II) NOUVEAU(0): [0x0d8c] 0x00000000
(II) NOUVEAU(0): [0x0d8d] 0x00000000
(II) NOUVEAU(0): [0x0d8e] 0x00000000
(II) NOUVEAU(0): [0x0d8f] 0x3f800000
(II) NOUVEAU(0): [0x0d90] 0x00080394
(II) NOUVEAU(0): [0x0d91] 0x00000000
(II) NOUVEAU(0): [0x0d92] 0x47800000
(II) NOUVEAU(0): [0x0d93] 0x001006e8
(II) NOUVEAU(0): [0x0d94] 0xc5000000
(II) NOUVEAU(0): [0x0d95] 0xc5000000
(II) NOUVEAU(0): [0x0d96] 0x00000000
(II) NOUVEAU(0): [0x0d97] 0x00000000
(II) NOUVEAU(0): [0x0d98] 0x00040218

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0d99] 0x0064e400
(II) NOUVEAU(0): [0x0d9a] 0x00040230
(II) NOUVEAU(0): [0x0d9b] 0x00400000
(II) NOUVEAU(0): [0x0d9c] 0x00040240
(II) NOUVEAU(0): [0x0d9d] 0x00020019
(II) NOUVEAU(0): [0x0d9e] 0x00040220
(II) NOUVEAU(0): [0x0d9f] 0x33141951
(II) NOUVEAU(0): [0x0da0] 0x00040228
(II) NOUVEAU(0): [0x0da1] 0x40000000
(II) NOUVEAU(0): [0x0da2] 0x00040248
(II) NOUVEAU(0): [0x0da3] 0x11000000
(II) NOUVEAU(0): [0x0da4] 0x00180260
(II) NOUVEAU(0): [0x0da5] 0x18301010
(II) NOUVEAU(0): [0x0da6] 0x00000000
(II) NOUVEAU(0): [0x0da7] 0x08200000
(II) NOUVEAU(0): [0x0da8] 0x00000000
(II) NOUVEAU(0): [0x0da9] 0x00000000
(II) NOUVEAU(0): [0x0daa] 0x00000000
(II) NOUVEAU(0): [0x0dab] 0x00080344
(II) NOUVEAU(0): [0x0dac] 0x00000001
(II) NOUVEAU(0): [0x0dad] 0x00000303
(II) NOUVEAU(0): [0x0dae] 0x00040304
(II) NOUVEAU(0): [0x0daf] 0x00000001
(II) NOUVEAU(0): [0x0db0] 0x00040dfc
(II) NOUVEAU(0): [0x0db1] 0x00000008
(II) NOUVEAU(0): [0x0db2] 0x00080c90
(II) NOUVEAU(0): [0x0db3] 0x00000000
(II) NOUVEAU(0): [0x0db4] 0x00000000
(II) NOUVEAU(0): [0x0db5] 0x000c0c00
(II) NOUVEAU(0): [0x0db6] 0x00000000
(II) NOUVEAU(0): [0x0db7] 0x00000000
(II) NOUVEAU(0): [0x0db8] 0x00000000
(II) NOUVEAU(0): [0x0db9] 0x00080c90
(II) NOUVEAU(0): [0x0dba] 0x40000000
(II) NOUVEAU(0): [0x0dbb] 0x00000000
(II) NOUVEAU(0): [0x0dbc] 0x000c0c00
(II) NOUVEAU(0): [0x0dbd] 0x40000000

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0dbe] 0x00000000
(II) NOUVEAU(0): [0x0dbf] 0x00000000
(II) NOUVEAU(0): [0x0dc0] 0x00080c90
(II) NOUVEAU(0): [0x0dc1] 0x40000000
(II) NOUVEAU(0): [0x0dc2] 0x41c80000
(II) NOUVEAU(0): [0x0dc3] 0x000c0c00
(II) NOUVEAU(0): [0x0dc4] 0x40000000
(II) NOUVEAU(0): [0x0dc5] 0x41c80000
(II) NOUVEAU(0): [0x0dc6] 0x00000000
(II) NOUVEAU(0): [0x0dc7] 0x00080c90
(II) NOUVEAU(0): [0x0dc8] 0x00000000
(II) NOUVEAU(0): [0x0dc9] 0x41c80000
(II) NOUVEAU(0): [0x0dca] 0x000c0c00
(II) NOUVEAU(0): [0x0dcb] 0x00000000
(II) NOUVEAU(0): [0x0dcc] 0x41c80000
(II) NOUVEAU(0): [0x0dcd] 0x00000000
(II) NOUVEAU(0): [0x0dce] 0x00040dfc
(II) NOUVEAU(0): [0x0dcf] 0x00000000
(II) NOUVEAU(0): [0x0dd0] 0x000442fc
(II) NOUVEAU(0): [0x0dd1] 0x00000003
(II) NOUVEAU(0): [0x0dd2] 0x00102300
(II) NOUVEAU(0): [0x0dd3] 0x0000000b
(II) NOUVEAU(0): [0x0dd4] 0x00400040
(II) NOUVEAU(0): [0x0dd5] 0x0064dd00
(II) NOUVEAU(0): [0x0dd6] 0x0064dd00
(II) NOUVEAU(0): [0x0dd7] 0x00044300
(II) NOUVEAU(0): [0x0dd8] 0x00000003
(II) NOUVEAU(0): [0x0dd9] 0x000443fc
(II) NOUVEAU(0): [0x0dda] 0x00c1b7
(II) NOUVEAU(0): [0x0ddb] 0x00084400
(II) NOUVEAU(0): [0x0ddc] 0x00000018
(II) NOUVEAU(0): [0x0ddd] 0x00020001
(II) NOUVEAU(0): [0x0dde] 0x0004c2fc
(II) NOUVEAU(0): [0x0ddf] 0x00000003
(II) NOUVEAU(0): [0x0de0] 0x00102300
(II) NOUVEAU(0): [0x0de1] 0x0000000a
(II) NOUVEAU(0): [0x0de2] 0x14000040

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

(II) NOUVEAU(0): [0x0de3] 0x0064dd00
(II) NOUVEAU(0): [0x0de4] 0x00009000
(II) NOUVEAU(0): [0x0de5] 0x000cc300
(II) NOUVEAU(0): [0x0de6] 0x00000000
(II) NOUVEAU(0): [0x0de7] 0x0208026a
(II) NOUVEAU(0): [0x0de8] 0x00190002
(II) NOUVEAU(0): [0x0de9] 0x000442fc
(II) NOUVEAU(0): [0x0dea] 0x00000003
(II) NOUVEAU(0): [0x0deb] 0x00102300
(II) NOUVEAU(0): [0x0dec] 0x0000000b
(II) NOUVEAU(0): [0x0ded] 0x00400040
(II) NOUVEAU(0): [0x0dee] 0x0068b800
(II) NOUVEAU(0): [0x0def] 0x0068b800
(II) NOUVEAU(0): [0x0df0] 0x00044300
(II) NOUVEAU(0): [0x0df1] 0x00000003
(II) NOUVEAU(0): [0x0df2] 0x000443fc
(II) NOUVEAU(0): [0x0df3] 0x00ffffff
(II) NOUVEAU(0): [0x0df4] 0x00084400
(II) NOUVEAU(0): [0x0df5] 0x00000000
(II) NOUVEAU(0): [0x0df6] 0x00020113
(II) NOUVEAU(0): [0x0df7] 0x00084400
(II) NOUVEAU(0): [0x0df8] 0x00000113
(II) NOUVEAU(0): [0x0df9] 0x00010002
(II) NOUVEAU(0): [0x0dfa] 0x0008e184
(II) NOUVEAU(0): [0x0dfb] 0xd8000002
(II) NOUVEAU(0): [0x0dfc] 0xd8000001
(II) NOUVEAU(0): [0x0dfd] 0x0020e30c
(II) NOUVEAU(0): [0x0dfe] 0x00000000
(II) NOUVEAU(0): [0x0dff] 0x0064dd00
(II) NOUVEAU(0): [0x0e00] 0x00000007
(II) NOUVEAU(0): [0x0e01] 0x00000040
(II) NOUVEAU(0): [0x0e02] 0x00000007
(II) NOUVEAU(0): [0x0e03] 0x0000000b
(II) NOUVEAU(0): [0x0e04] 0x00000101
(II) NOUVEAU(0): [0x0e05] 0x00000000
(II) NOUVEAU(0): [0x0e06] 0x0004e104
(II) NOUVEAU(0): [0x0e07] 0x00000000

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0): [0x0e08] 0x0004e100
(II) NOUVEAU(0): [0x0e09] 0x00000000
(II) NOUVEAU(0): [0x0e0a] 0x00000000
(II) NOUVEAU(0): [0x0e0b] 0x00080200
(II) NOUVEAU(0): [0x0e0c] 0x08000000
(II) NOUVEAU(0): [0x0e0d] 0x08000000
(II) NOUVEAU(0): [0x0e0e] 0x000402b4
(II) NOUVEAU(0): [0x0e0f] 0x00000000
(II) NOUVEAU(0): [0x0e10] 0x000402c0
(II) NOUVEAU(0): [0x0e11] 0xffff0800
(II) NOUVEAU(0): [0x0e12] 0x000402e0
(II) NOUVEAU(0): [0x0e13] 0xffff0800
(II) NOUVEAU(0): End of fifo dump
```

Fatal server error:

DMA queue hang: dmaPut=e0a, current=c85, status=0

NVLeaveVT is called

Setting owner: 0x0

Setting owner: 0x3

Restoring TMDS timings, before restoring anything else

The clock seems to be 27996 kHz

```
(II) NOUVEAU(0): 0xE292: Parsing digital output script table
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x57, Head: 0x00, Data: 0x00
(II) NOUVEAU(0): Indexed IO write: Port: 0x03D4, Index: 0x58, Head: 0x00, Data: 0x01
(II) NOUVEAU(0): 0xE292: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xE292: Reg: 0x00680524, Mask: 0xFFFEFFFF, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x00680524, Data: 0x00000000
(II) NOUVEAU(0): Write: Reg: 0x00680524, Data: 0x00000000
(II) NOUVEAU(0): 0xE29F: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xE29F: Reg: 0x00680848, Mask: 0x7FFFFFFF, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x00680848, Data: 0x11000011
(II) NOUVEAU(0): Write: Reg: 0x00680848, Data: 0x11000011
(II) NOUVEAU(0): 0xE2AC: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xE2AC: Reg: 0x00680880, Mask: 0xDFFFFFFF, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x00680880, Data: 0x31101111
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0):          Write: Reg: 0x00680880, Data: 0x11101111
(II) NOUVEAU(0): 0xE2B9: [ (0x49) - INIT_INDEX_ADDRESS_LATCHED ]
(II) NOUVEAU(0): 0xE2B9: ControlReg: 0x006808B0, DataReg: 0x006808B4, Mask: 0x00000000, Data: 0x00000000
(II) NOUVEAU(0): 0xE2B9: Address: 0x04, Data: 0x80
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000080
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x00010004
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x00000004
(II) NOUVEAU(0): 0xE2B9: Address: 0x2F, Data: 0x10
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000010
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x00000004
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x0000002F
(II) NOUVEAU(0): 0xE2B9: Address: 0x00, Data: 0x6C
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x0000006C
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x0000002F
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x00000000
(II) NOUVEAU(0): 0xE2B9: Address: 0x01, Data: 0x78
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000078
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x00000000
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x00000001
(II) NOUVEAU(0): 0xE2B9: Address: 0x02, Data: 0xA9
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x000000A9
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x00000001
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x00000002
(II) NOUVEAU(0): 0xE2B9: Address: 0x03, Data: 0x04
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000004
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x00000002
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x00000003
(II) NOUVEAU(0): 0xE2B9: Address: 0x2E, Data: 0x05
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000005
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x00000003
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x0000002E
(II) NOUVEAU(0): 0xE2B9: Address: 0x30, Data: 0x00
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000000
(II) NOUVEAU(0):          Read: Reg: 0x006808B0, Data: 0x0000002E
(II) NOUVEAU(0):          Write: Reg: 0x006808B0, Data: 0x00000030
(II) NOUVEAU(0): 0xE2B9: Address: 0x31, Data: 0x00
(II) NOUVEAU(0):          Write: Reg: 0x006808B4, Data: 0x00000000
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0):      Read:  Reg: 0x006808B0, Data: 0x00000030
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x00000031
(II) NOUVEAU(0): 0xE2B9: Address: 0x32, Data: 0x00
(II) NOUVEAU(0):      Write: Reg: 0x006808B4, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x006808B0, Data: 0x00000031
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x00000032
(II) NOUVEAU(0): 0xE2B9: Address: 0x33, Data: 0x00
(II) NOUVEAU(0):      Write: Reg: 0x006808B4, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x006808B0, Data: 0x00000032
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x00000033
(II) NOUVEAU(0): 0xE2B9: Address: 0x2F, Data: 0x04
(II) NOUVEAU(0):      Write: Reg: 0x006808B4, Data: 0x00000004
(II) NOUVEAU(0):      Read:  Reg: 0x006808B0, Data: 0x00000033
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x0000002F
(II) NOUVEAU(0): 0xE2E3: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0):      Write: Reg: 0x006808B4, Data: 0x00000080
(II) NOUVEAU(0):      Write: Reg: 0x006808B0, Data: 0x00000004
(II) NOUVEAU(0):      Write: Reg: 0x006808BC, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x006808B8, Data: 0x00000004
Restoring TMDS timings, before restoring anything else
The clock seems to be 27996 kHz
(II) NOUVEAU(0): 0xE351: Parsing digital output script table
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x44, Head: 0x00, Data: 0x03
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x57, Head: 0x01, Data: 0x00
(II) NOUVEAU(0):      Indexed IO write: Port: 0x03D4, Index: 0x58, Head: 0x01, Data: 0x03
(II) NOUVEAU(0): 0xE351: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xE351: Reg: 0x00680524, Mask: 0xFFFFFFFF, Data: 0x00040000
(II) NOUVEAU(0):      Read:  Reg: 0x00680524, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x00680524, Data: 0x00040000
(II) NOUVEAU(0): 0xE35E: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xE35E: Reg: 0x00682848, Mask: 0x7FFFFFFF, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x00682848, Data: 0x21000022
(II) NOUVEAU(0):      Write: Reg: 0x00682848, Data: 0x21000022
(II) NOUVEAU(0): 0xE36B: [ (0x6E) - INIT_NV_REG ]
(II) NOUVEAU(0): 0xE36B: Reg: 0x00682880, Mask: 0xCFFFFFFF, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x00682880, Data: 0x01101100
(II) NOUVEAU(0):      Write: Reg: 0x00682880, Data: 0x01101100
```


File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0): 0xE378: [ (0x49) - INIT_INDEX_ADDRESS_LATCHED ]
(II) NOUVEAU(0): 0xE378: ControlReg: 0x006828B0, DataReg: 0x006828B4, Mask: 0x00000000, Data: 0x00000000
(II) NOUVEAU(0): 0xE378: Address: 0x04, Data: 0x80
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000080
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00010004
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x00000004
(II) NOUVEAU(0): 0xE378: Address: 0x2F, Data: 0x10
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000010
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00000004
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x0000002F
(II) NOUVEAU(0): 0xE378: Address: 0x00, Data: 0x6C
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x0000006C
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x0000002F
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x00000000
(II) NOUVEAU(0): 0xE378: Address: 0x01, Data: 0x78
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000078
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00000000
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x00000001
(II) NOUVEAU(0): 0xE378: Address: 0x02, Data: 0xA9
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x000000A9
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00000001
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x00000002
(II) NOUVEAU(0): 0xE378: Address: 0x03, Data: 0x04
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000004
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00000002
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x00000003
(II) NOUVEAU(0): 0xE378: Address: 0x2E, Data: 0x05
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000005
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00000003
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x0000002E
(II) NOUVEAU(0): 0xE378: Address: 0x30, Data: 0x00
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x0000002E
(II) NOUVEAU(0): Write: Reg: 0x006828B0, Data: 0x00000030
(II) NOUVEAU(0): 0xE378: Address: 0x31, Data: 0x00
(II) NOUVEAU(0): Write: Reg: 0x006828B4, Data: 0x00000000
(II) NOUVEAU(0): Read: Reg: 0x006828B0, Data: 0x00000030
```

File: /home/careys/nouveau/017A-NV17GL-half/20080202/r2/Xorg.0.log

```
(II) NOUVEAU(0):      Write: Reg: 0x006828B0, Data: 0x00000031
(II) NOUVEAU(0): 0xE378: Address: 0x32, Data: 0x00
(II) NOUVEAU(0):      Write: Reg: 0x006828B4, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x006828B0, Data: 0x00000031
(II) NOUVEAU(0):      Write: Reg: 0x006828B0, Data: 0x00000032
(II) NOUVEAU(0): 0xE378: Address: 0x33, Data: 0x00
(II) NOUVEAU(0):      Write: Reg: 0x006828B4, Data: 0x00000000
(II) NOUVEAU(0):      Read:  Reg: 0x006828B0, Data: 0x00000032
(II) NOUVEAU(0):      Write: Reg: 0x006828B0, Data: 0x00000033
(II) NOUVEAU(0): 0xE378: Address: 0x2F, Data: 0x04
(II) NOUVEAU(0):      Write: Reg: 0x006828B4, Data: 0x00000004
(II) NOUVEAU(0):      Read:  Reg: 0x006828B0, Data: 0x00000033
(II) NOUVEAU(0):      Write: Reg: 0x006828B0, Data: 0x0000002F
(II) NOUVEAU(0): 0xE3A2: [ (0x71) - INIT_DONE ]
(II) NOUVEAU(0):      Write: Reg: 0x006828B4, Data: 0x00000080
(II) NOUVEAU(0):      Write: Reg: 0x006828B0, Data: 0x00000004
(II) NOUVEAU(0):      Write: Reg: 0x006828BC, Data: 0x00000000
(II) NOUVEAU(0):      Write: Reg: 0x006828B8, Data: 0x00000004
nv_crtc_restore is called for CRTC 0
Setting owner: 0x0
writing sel_clk 00000000
writing vpll1_a 0004DB07
writing pllsel 00000500
nv_crtc_restore is called for CRTC 1
Setting owner: 0x3
writing sel_clk 00000000
writing vpll2_a 0004DB07
writing pllsel 00000500
nv_output_restore is called
nv_output_restore is called
nv_output_restore is called
nv_output_restore is called
Setting owner: 0x0
Setting owner: 0x3
Restoring CRTC_OWNER to 7
```