<list xmlns:lldp="lldp" xmlns:lshw="lshw">

<lldp:lldp label="LLDP neighbors">

<lldp:interface label="Interface" name="eth0" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/1</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/1</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth0" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/2</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/2</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth0" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/3</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/3</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth0" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/4</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/4</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth1" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/5</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/5</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth1" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/6</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/6</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth1" via="CDPv2" rid="1" age="0 day, 00:00:28">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/7</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/7</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

<lldp:interface label="Interface" name="eth1" via="CDPv2" rid="1" age="0 day, 00:00:27">

<lldp:chassis label="Chassis">

<lldp:id label="ChassisID" type="local">nexus5k-esx(FOX1827G1QP)</lldp:id>

<lldp:name label="SysName">nexus5k-esx(FOX1827G1QP)</lldp:name>

<lldp:descr label="SysDescr">N5K-C5596T running on

Cisco Nexus Operating System (NX-OS) Software, Version 7.0(1)N1(1)</lldp:descr>

<lldp:mgmt-ip label="MgmtIP">10.104.216.130</lldp:mgmt-ip>

<lldp:capability label="Capability" type="Bridge" enabled="on"/>

</lldp:chassis>

<lldp:port label="Port">

<lldp:id label="PortID" type="ifname">Ethernet2/8</lldp:id>

<lldp:descr label="PortDescr">Ethernet2/8</lldp:descr>

</lldp:port>

<lldp:vlan label="VLAN" vlan-id="201" pvid="yes">VLAN #201</lldp:vlan>

</lldp:interface>

</lldp:lldp>

<lshw:list>

<lshw:node id="stoneyford" claimed="true" class="system" handle="DMI:0001">

<lshw:description>Rack Mount Chassis</lshw:description>

<lshw:product>UCSC-C3K-M4SRB ()</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:version>A0</lshw:version>

<lshw:serial>FCH200374FS</lshw:serial>

<lshw:width units="bits">64</lshw:width>

<lshw:configuration>

<lshw:setting id="administrator\_password" value="disabled"/>

<lshw:setting id="boot" value="normal"/>

<lshw:setting id="chassis" value="rackmount"/>

<lshw:setting id="frontpanel\_password" value="disabled"/>

<lshw:setting id="keyboard\_password" value="disabled"/>

<lshw:setting id="power-on\_password" value="disabled"/>

<lshw:setting id="uuid" value="2055AB14-BB05-4F34-8532-F4945F155B8D"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="smbios-3.0">SMBIOS version 3.0</lshw:capability>

<lshw:capability id="dmi-3.0">DMI version 3.0</lshw:capability>

<lshw:capability id="vsyscall32">32-bit processes</lshw:capability>

</lshw:capabilities>

<lshw:node id="core" claimed="true" class="bus" handle="DMI:0002">

<lshw:description>Motherboard</lshw:description>

<lshw:product>UCSC-C3K-M4SRB</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:version>73-17622-03</lshw:version>

<lshw:serial>FCH200374FS</lshw:serial>

<lshw:node id="firmware" claimed="true" class="memory" handle="">

<lshw:description>BIOS</lshw:description>

<lshw:vendor>Cisco Systems, Inc.</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:version>C3X60M4.2.0.12.25.0702162216</lshw:version>

<lshw:date>07/02/2016</lshw:date>

<lshw:size units="bytes">65536</lshw:size>

<lshw:capacity units="bytes">9371648</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="pci">PCI bus</lshw:capability>

<lshw:capability id="upgrade">BIOS EEPROM can be upgraded</lshw:capability>

<lshw:capability id="shadowing">BIOS shadowing</lshw:capability>

<lshw:capability id="cdboot">Booting from CD-ROM/DVD</lshw:capability>

<lshw:capability id="bootselect">Selectable boot path</lshw:capability>

<lshw:capability id="socketedrom">BIOS ROM is socketed</lshw:capability>

<lshw:capability id="edd">Enhanced Disk Drive extensions</lshw:capability>

<lshw:capability id="int13floppy1200">5.25" 1.2MB floppy</lshw:capability>

<lshw:capability id="int13floppy720">3.5" 720KB floppy</lshw:capability>

<lshw:capability id="int13floppy2880">3.5" 2.88MB floppy</lshw:capability>

<lshw:capability id="int5printscreen">Print Screen key</lshw:capability>

<lshw:capability id="int9keyboard">i8042 keyboard controller</lshw:capability>

<lshw:capability id="int14serial">INT14 serial line control</lshw:capability>

<lshw:capability id="int17printer">INT17 printer control</lshw:capability>

<lshw:capability id="acpi">ACPI</lshw:capability>

<lshw:capability id="usb">USB legacy emulation</lshw:capability>

<lshw:capability id="biosbootspecification">BIOS boot specification</lshw:capability>

<lshw:capability id="uefi">UEFI specification is supported</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="memory" claimed="true" class="memory" handle="DMI:0022">

<lshw:description>System Memory</lshw:description>

<lshw:physid>22</lshw:physid>

<lshw:slot>System board or motherboard</lshw:slot>

<lshw:size units="bytes">103079215104</lshw:size>

<lshw:node id="bank:0" claimed="true" class="memory" handle="DMI:0024">

<lshw:description>DIMM 2400 MHz (0.4 ns)</lshw:description>

<lshw:product>HMA82GR7MFR4N-UH</lshw:product>

<lshw:vendor>Hynix Semiconductor (Hyundai Electronics)</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:serial>80E5BEBB</lshw:serial>

<lshw:slot>DIMM\_A1</lshw:slot>

<lshw:size units="bytes">17179869184</lshw:size>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">2400000000</lshw:clock>

</lshw:node>

<lshw:node id="bank:1" claimed="true" class="memory" handle="DMI:0025">

<lshw:description>DIMM 2400 MHz (0.4 ns)</lshw:description>

<lshw:product>HMA82GR7MFR4N-UH</lshw:product>

<lshw:vendor>Hynix Semiconductor (Hyundai Electronics)</lshw:vendor>

<lshw:physid>1</lshw:physid>

<lshw:serial>80E5BEC8</lshw:serial>

<lshw:slot>DIMM\_A2</lshw:slot>

<lshw:size units="bytes">17179869184</lshw:size>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">2400000000</lshw:clock>

</lshw:node>

<lshw:node id="bank:2" claimed="true" class="memory" handle="DMI:0026">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>2</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_B1</lshw:slot>

</lshw:node>

<lshw:node id="bank:3" claimed="true" class="memory" handle="DMI:0027">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>3</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_B2</lshw:slot>

</lshw:node>

<lshw:node id="bank:4" claimed="true" class="memory" handle="DMI:0028">

<lshw:description>DIMM 2133 MHz (0.5 ns)</lshw:description>

<lshw:product>HMA42GR7MFR4N-TFT1</lshw:product>

<lshw:vendor>Hynix Semiconductor (Hyundai Electronics)</lshw:vendor>

<lshw:physid>4</lshw:physid>

<lshw:serial>10375DE3</lshw:serial>

<lshw:slot>DIMM\_C1</lshw:slot>

<lshw:size units="bytes">17179869184</lshw:size>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">2133000000</lshw:clock>

</lshw:node>

<lshw:node id="bank:5" claimed="true" class="memory" handle="DMI:0029">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>5</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_C2</lshw:slot>

</lshw:node>

<lshw:node id="bank:6" claimed="true" class="memory" handle="DMI:002A">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>6</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_D1</lshw:slot>

</lshw:node>

<lshw:node id="bank:7" claimed="true" class="memory" handle="DMI:002B">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>7</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_D2</lshw:slot>

</lshw:node>

<lshw:node id="bank:8" claimed="true" class="memory" handle="DMI:002C">

<lshw:description>DIMM 2133 MHz (0.5 ns)</lshw:description>

<lshw:product>HMA42GR7MFR4N-TFT1</lshw:product>

<lshw:vendor>Hynix Semiconductor (Hyundai Electronics)</lshw:vendor>

<lshw:physid>8</lshw:physid>

<lshw:serial>10375DDC</lshw:serial>

<lshw:slot>DIMM\_E1</lshw:slot>

<lshw:size units="bytes">17179869184</lshw:size>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">2133000000</lshw:clock>

</lshw:node>

<lshw:node id="bank:9" claimed="true" class="memory" handle="DMI:002D">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>9</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_E2</lshw:slot>

</lshw:node>

<lshw:node id="bank:10" claimed="true" class="memory" handle="DMI:002E">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>a</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_F1</lshw:slot>

</lshw:node>

<lshw:node id="bank:11" claimed="true" class="memory" handle="DMI:002F">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>b</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_F2</lshw:slot>

</lshw:node>

<lshw:node id="bank:12" claimed="true" class="memory" handle="DMI:0030">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>c</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_G1</lshw:slot>

</lshw:node>

<lshw:node id="bank:13" claimed="true" class="memory" handle="DMI:0031">

<lshw:description>[empty]</lshw:description>

<lshw:product>NO DIMM</lshw:product>

<lshw:vendor>NO DIMM</lshw:vendor>

<lshw:physid>d</lshw:physid>

<lshw:serial>NO DIMM</lshw:serial>

<lshw:slot>DIMM\_G2</lshw:slot>

</lshw:node>

<lshw:node id="bank:14" claimed="true" class="memory" handle="DMI:0032">

<lshw:description>DIMM 2400 MHz (0.4 ns)</lshw:description>

<lshw:product>HMA82GR7MFR4N-UH</lshw:product>

<lshw:vendor>Hynix Semiconductor (Hyundai Electronics)</lshw:vendor>

<lshw:physid>e</lshw:physid>

<lshw:serial>80E5BEC6</lshw:serial>

<lshw:slot>DIMM\_H1</lshw:slot>

<lshw:size units="bytes">17179869184</lshw:size>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">2400000000</lshw:clock>

</lshw:node>

<lshw:node id="bank:15" claimed="true" class="memory" handle="DMI:0033">

<lshw:description>DIMM 2400 MHz (0.4 ns)</lshw:description>

<lshw:product>HMA82GR7MFR4N-UH</lshw:product>

<lshw:vendor>Hynix Semiconductor (Hyundai Electronics)</lshw:vendor>

<lshw:physid>f</lshw:physid>

<lshw:serial>80E5BF09</lshw:serial>

<lshw:slot>DIMM\_H2</lshw:slot>

<lshw:size units="bytes">17179869184</lshw:size>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">2400000000</lshw:clock>

</lshw:node>

</lshw:node>

<lshw:node id="cache:0" claimed="true" class="memory" handle="DMI:0034">

<lshw:description>L1 cache</lshw:description>

<lshw:physid>34</lshw:physid>

<lshw:slot>CPU Internal L1</lshw:slot>

<lshw:size units="bytes">262144</lshw:size>

<lshw:capacity units="bytes">262144</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="data">Data cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:1" claimed="true" class="memory" handle="DMI:0035">

<lshw:description>L1 cache</lshw:description>

<lshw:physid>35</lshw:physid>

<lshw:slot>CPU Internal L1</lshw:slot>

<lshw:size units="bytes">262144</lshw:size>

<lshw:capacity units="bytes">262144</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="instruction">Instruction cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:2" claimed="true" class="memory" handle="DMI:0036">

<lshw:description>L2 cache</lshw:description>

<lshw:physid>36</lshw:physid>

<lshw:slot>CPU Internal L2</lshw:slot>

<lshw:size units="bytes">2097152</lshw:size>

<lshw:capacity units="bytes">2097152</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="unified">Unified cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:3" claimed="true" class="memory" handle="DMI:0037">

<lshw:description>L3 cache</lshw:description>

<lshw:physid>37</lshw:physid>

<lshw:slot>CPU Internal L3</lshw:slot>

<lshw:size units="bytes">20971520</lshw:size>

<lshw:capacity units="bytes">20971520</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="unified">Unified cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cpu:0" claimed="true" class="processor" handle="DMI:0038">

<lshw:description>CPU</lshw:description>

<lshw:product>Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz</lshw:product>

<lshw:vendor>Intel Corp.</lshw:vendor>

<lshw:physid>38</lshw:physid>

<lshw:businfo>cpu@0</lshw:businfo>

<lshw:version>Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz</lshw:version>

<lshw:slot>CPU1</lshw:slot>

<lshw:size units="Hz">1200000000</lshw:size>

<lshw:capacity units="Hz">4000000000</lshw:capacity>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">100000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="cores" value="8"/>

<lshw:setting id="enabledcores" value="8"/>

<lshw:setting id="threads" value="16"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="x86-64">64bits extensions (x86-64)</lshw:capability>

<lshw:capability id="fpu">mathematical co-processor</lshw:capability>

<lshw:capability id="fpu\_exception">FPU exceptions reporting</lshw:capability>

<lshw:capability id="wp"/>

<lshw:capability id="vme">virtual mode extensions</lshw:capability>

<lshw:capability id="de">debugging extensions</lshw:capability>

<lshw:capability id="pse">page size extensions</lshw:capability>

<lshw:capability id="tsc">time stamp counter</lshw:capability>

<lshw:capability id="msr">model-specific registers</lshw:capability>

<lshw:capability id="pae">4GB+ memory addressing (Physical Address Extension)</lshw:capability>

<lshw:capability id="mce">machine check exceptions</lshw:capability>

<lshw:capability id="cx8">compare and exchange 8-byte</lshw:capability>

<lshw:capability id="apic">on-chip advanced programmable interrupt controller (APIC)</lshw:capability>

<lshw:capability id="sep">fast system calls</lshw:capability>

<lshw:capability id="mtrr">memory type range registers</lshw:capability>

<lshw:capability id="pge">page global enable</lshw:capability>

<lshw:capability id="mca">machine check architecture</lshw:capability>

<lshw:capability id="cmov">conditional move instruction</lshw:capability>

<lshw:capability id="pat">page attribute table</lshw:capability>

<lshw:capability id="pse36">36-bit page size extensions</lshw:capability>

<lshw:capability id="clflush"/>

<lshw:capability id="dts">debug trace and EMON store MSRs</lshw:capability>

<lshw:capability id="acpi">thermal control (ACPI)</lshw:capability>

<lshw:capability id="mmx">multimedia extensions (MMX)</lshw:capability>

<lshw:capability id="fxsr">fast floating point save/restore</lshw:capability>

<lshw:capability id="sse">streaming SIMD extensions (SSE)</lshw:capability>

<lshw:capability id="sse2">streaming SIMD extensions (SSE2)</lshw:capability>

<lshw:capability id="ss">self-snoop</lshw:capability>

<lshw:capability id="ht">HyperThreading</lshw:capability>

<lshw:capability id="tm">thermal interrupt and status</lshw:capability>

<lshw:capability id="pbe">pending break event</lshw:capability>

<lshw:capability id="syscall">fast system calls</lshw:capability>

<lshw:capability id="nx">no-execute bit (NX)</lshw:capability>

<lshw:capability id="pdpe1gb"/>

<lshw:capability id="rdtscp"/>

<lshw:capability id="constant\_tsc"/>

<lshw:capability id="arch\_perfmon"/>

<lshw:capability id="pebs"/>

<lshw:capability id="bts"/>

<lshw:capability id="rep\_good"/>

<lshw:capability id="nopl"/>

<lshw:capability id="xtopology"/>

<lshw:capability id="nonstop\_tsc"/>

<lshw:capability id="aperfmperf"/>

<lshw:capability id="eagerfpu"/>

<lshw:capability id="pni"/>

<lshw:capability id="pclmulqdq"/>

<lshw:capability id="dtes64"/>

<lshw:capability id="monitor"/>

<lshw:capability id="ds\_cpl"/>

<lshw:capability id="vmx"/>

<lshw:capability id="smx"/>

<lshw:capability id="est"/>

<lshw:capability id="tm2"/>

<lshw:capability id="ssse3"/>

<lshw:capability id="fma"/>

<lshw:capability id="cx16"/>

<lshw:capability id="xtpr"/>

<lshw:capability id="pdcm"/>

<lshw:capability id="pcid"/>

<lshw:capability id="dca"/>

<lshw:capability id="sse4\_1"/>

<lshw:capability id="sse4\_2"/>

<lshw:capability id="x2apic"/>

<lshw:capability id="movbe"/>

<lshw:capability id="popcnt"/>

<lshw:capability id="tsc\_deadline\_timer"/>

<lshw:capability id="aes"/>

<lshw:capability id="xsave"/>

<lshw:capability id="avx"/>

<lshw:capability id="f16c"/>

<lshw:capability id="rdrand"/>

<lshw:capability id="lahf\_lm"/>

<lshw:capability id="abm"/>

<lshw:capability id="3dnowprefetch"/>

<lshw:capability id="ida"/>

<lshw:capability id="arat"/>

<lshw:capability id="epb"/>

<lshw:capability id="xsaveopt"/>

<lshw:capability id="pln"/>

<lshw:capability id="pts"/>

<lshw:capability id="dtherm"/>

<lshw:capability id="tpr\_shadow"/>

<lshw:capability id="vnmi"/>

<lshw:capability id="flexpriority"/>

<lshw:capability id="ept"/>

<lshw:capability id="vpid"/>

<lshw:capability id="fsgsbase"/>

<lshw:capability id="tsc\_adjust"/>

<lshw:capability id="bmi1"/>

<lshw:capability id="hle"/>

<lshw:capability id="avx2"/>

<lshw:capability id="smep"/>

<lshw:capability id="bmi2"/>

<lshw:capability id="erms"/>

<lshw:capability id="invpcid"/>

<lshw:capability id="rtm"/>

<lshw:capability id="rdseed"/>

<lshw:capability id="adx"/>

<lshw:capability id="smap"/>

<lshw:capability id="cpufreq">CPU Frequency scaling</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:4" claimed="true" class="memory" handle="DMI:0039">

<lshw:description>L1 cache</lshw:description>

<lshw:physid>39</lshw:physid>

<lshw:slot>CPU Internal L1</lshw:slot>

<lshw:size units="bytes">262144</lshw:size>

<lshw:capacity units="bytes">262144</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="data">Data cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:5" claimed="true" class="memory" handle="DMI:003A">

<lshw:description>L1 cache</lshw:description>

<lshw:physid>3a</lshw:physid>

<lshw:slot>CPU Internal L1</lshw:slot>

<lshw:size units="bytes">262144</lshw:size>

<lshw:capacity units="bytes">262144</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="instruction">Instruction cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:6" claimed="true" class="memory" handle="DMI:003B">

<lshw:description>L2 cache</lshw:description>

<lshw:physid>3b</lshw:physid>

<lshw:slot>CPU Internal L2</lshw:slot>

<lshw:size units="bytes">2097152</lshw:size>

<lshw:capacity units="bytes">2097152</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="unified">Unified cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cache:7" claimed="true" class="memory" handle="DMI:003C">

<lshw:description>L3 cache</lshw:description>

<lshw:physid>3c</lshw:physid>

<lshw:slot>CPU Internal L3</lshw:slot>

<lshw:size units="bytes">20971520</lshw:size>

<lshw:capacity units="bytes">20971520</lshw:capacity>

<lshw:capabilities>

<lshw:capability id="internal">Internal</lshw:capability>

<lshw:capability id="varies">Varies With Memory Address</lshw:capability>

<lshw:capability id="unified">Unified cache</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="cpu:1" claimed="true" class="processor" handle="DMI:003D">

<lshw:description>CPU</lshw:description>

<lshw:product>Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz</lshw:product>

<lshw:vendor>Intel Corp.</lshw:vendor>

<lshw:physid>3d</lshw:physid>

<lshw:businfo>cpu@1</lshw:businfo>

<lshw:version>Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz</lshw:version>

<lshw:slot>CPU2</lshw:slot>

<lshw:size units="Hz">1200000000</lshw:size>

<lshw:capacity units="Hz">4000000000</lshw:capacity>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">100000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="cores" value="8"/>

<lshw:setting id="enabledcores" value="8"/>

<lshw:setting id="threads" value="16"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="x86-64">64bits extensions (x86-64)</lshw:capability>

<lshw:capability id="fpu">mathematical co-processor</lshw:capability>

<lshw:capability id="fpu\_exception">FPU exceptions reporting</lshw:capability>

<lshw:capability id="wp"/>

<lshw:capability id="vme">virtual mode extensions</lshw:capability>

<lshw:capability id="de">debugging extensions</lshw:capability>

<lshw:capability id="pse">page size extensions</lshw:capability>

<lshw:capability id="tsc">time stamp counter</lshw:capability>

<lshw:capability id="msr">model-specific registers</lshw:capability>

<lshw:capability id="pae">4GB+ memory addressing (Physical Address Extension)</lshw:capability>

<lshw:capability id="mce">machine check exceptions</lshw:capability>

<lshw:capability id="cx8">compare and exchange 8-byte</lshw:capability>

<lshw:capability id="apic">on-chip advanced programmable interrupt controller (APIC)</lshw:capability>

<lshw:capability id="sep">fast system calls</lshw:capability>

<lshw:capability id="mtrr">memory type range registers</lshw:capability>

<lshw:capability id="pge">page global enable</lshw:capability>

<lshw:capability id="mca">machine check architecture</lshw:capability>

<lshw:capability id="cmov">conditional move instruction</lshw:capability>

<lshw:capability id="pat">page attribute table</lshw:capability>

<lshw:capability id="pse36">36-bit page size extensions</lshw:capability>

<lshw:capability id="clflush"/>

<lshw:capability id="dts">debug trace and EMON store MSRs</lshw:capability>

<lshw:capability id="acpi">thermal control (ACPI)</lshw:capability>

<lshw:capability id="mmx">multimedia extensions (MMX)</lshw:capability>

<lshw:capability id="fxsr">fast floating point save/restore</lshw:capability>

<lshw:capability id="sse">streaming SIMD extensions (SSE)</lshw:capability>

<lshw:capability id="sse2">streaming SIMD extensions (SSE2)</lshw:capability>

<lshw:capability id="ss">self-snoop</lshw:capability>

<lshw:capability id="ht">HyperThreading</lshw:capability>

<lshw:capability id="tm">thermal interrupt and status</lshw:capability>

<lshw:capability id="pbe">pending break event</lshw:capability>

<lshw:capability id="syscall">fast system calls</lshw:capability>

<lshw:capability id="nx">no-execute bit (NX)</lshw:capability>

<lshw:capability id="pdpe1gb"/>

<lshw:capability id="rdtscp"/>

<lshw:capability id="constant\_tsc"/>

<lshw:capability id="arch\_perfmon"/>

<lshw:capability id="pebs"/>

<lshw:capability id="bts"/>

<lshw:capability id="rep\_good"/>

<lshw:capability id="nopl"/>

<lshw:capability id="xtopology"/>

<lshw:capability id="nonstop\_tsc"/>

<lshw:capability id="aperfmperf"/>

<lshw:capability id="eagerfpu"/>

<lshw:capability id="pni"/>

<lshw:capability id="pclmulqdq"/>

<lshw:capability id="dtes64"/>

<lshw:capability id="monitor"/>

<lshw:capability id="ds\_cpl"/>

<lshw:capability id="vmx"/>

<lshw:capability id="smx"/>

<lshw:capability id="est"/>

<lshw:capability id="tm2"/>

<lshw:capability id="ssse3"/>

<lshw:capability id="fma"/>

<lshw:capability id="cx16"/>

<lshw:capability id="xtpr"/>

<lshw:capability id="pdcm"/>

<lshw:capability id="pcid"/>

<lshw:capability id="dca"/>

<lshw:capability id="sse4\_1"/>

<lshw:capability id="sse4\_2"/>

<lshw:capability id="x2apic"/>

<lshw:capability id="movbe"/>

<lshw:capability id="popcnt"/>

<lshw:capability id="tsc\_deadline\_timer"/>

<lshw:capability id="aes"/>

<lshw:capability id="xsave"/>

<lshw:capability id="avx"/>

<lshw:capability id="f16c"/>

<lshw:capability id="rdrand"/>

<lshw:capability id="lahf\_lm"/>

<lshw:capability id="abm"/>

<lshw:capability id="3dnowprefetch"/>

<lshw:capability id="ida"/>

<lshw:capability id="arat"/>

<lshw:capability id="epb"/>

<lshw:capability id="xsaveopt"/>

<lshw:capability id="pln"/>

<lshw:capability id="pts"/>

<lshw:capability id="dtherm"/>

<lshw:capability id="tpr\_shadow"/>

<lshw:capability id="vnmi"/>

<lshw:capability id="flexpriority"/>

<lshw:capability id="ept"/>

<lshw:capability id="vpid"/>

<lshw:capability id="fsgsbase"/>

<lshw:capability id="tsc\_adjust"/>

<lshw:capability id="bmi1"/>

<lshw:capability id="hle"/>

<lshw:capability id="avx2"/>

<lshw:capability id="smep"/>

<lshw:capability id="bmi2"/>

<lshw:capability id="erms"/>

<lshw:capability id="invpcid"/>

<lshw:capability id="rtm"/>

<lshw:capability id="rdseed"/>

<lshw:capability id="adx"/>

<lshw:capability id="smap"/>

<lshw:capability id="cpufreq">CPU Frequency scaling</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="pci:0" claimed="true" class="bridge" handle="PCIBUS:0000:00">

<lshw:description>Host bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DMI2</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>100</lshw:physid>

<lshw:businfo>pci@0000:00:00.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:node id="pci:0" claimed="true" class="bridge" handle="PCIBUS:0000:01">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1</lshw:physid>

<lshw:businfo>pci@0000:00:01.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="90"/>

</lshw:resources>

</lshw:node>

<lshw:node id="pci:1" claimed="true" class="bridge" handle="PCIBUS:0000:02">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1.1</lshw:physid>

<lshw:businfo>pci@0000:00:01.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="91"/>

</lshw:resources>

</lshw:node>

<lshw:node id="pci:2" claimed="true" class="bridge" handle="PCIBUS:0000:03">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 2</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2</lshw:physid>

<lshw:businfo>pci@0000:00:02.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="92"/>

<lshw:resource type="ioport" value="4000(size=8192)"/>

<lshw:resource type="memory" value="c6900000-c6efffff"/>

</lshw:resources>

<lshw:node id="pci" claimed="true" class="bridge" handle="PCIBUS:0000:04">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC 1300 PCIe Upstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:03:00.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="97"/>

<lshw:resource type="ioport" value="4000(size=8192)"/>

<lshw:resource type="memory" value="c6900000-c6efffff"/>

</lshw:resources>

<lshw:node id="pci:0" claimed="true" class="bridge" handle="PCIBUS:0000:05">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Downstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:04:00.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="iomemory" value="f00-eff"/>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="memory" value="c6e00000-c6e03fff"/>

<lshw:resource type="memory" value="c6d00000-c6dfffff"/>

</lshw:resources>

<lshw:node id="generic" class="generic" handle="PCI:0000:05:00.0">

<lshw:description>Unclassified device</lshw:description>

<lshw:product>VIC Management Controller</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:05:00.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="msix">MSI-X</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="memory" value="c6d02000-c6d02fff"/>

<lshw:resource type="memory" value="c6d00000-c6d01fff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="pci:1" claimed="true" class="bridge" handle="PCIBUS:0000:06">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Downstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>1</lshw:physid>

<lshw:businfo>pci@0000:04:01.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="iomemory" value="50400-503ff"/>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="ioport" value="4000(size=8192)"/>

<lshw:resource type="memory" value="c6900000-c6cfffff"/>

</lshw:resources>

<lshw:node id="pci" claimed="true" class="bridge" handle="PCIBUS:0000:07">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Upstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:06:00.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="ioport" value="4000(size=8192)"/>

<lshw:resource type="memory" value="c6900000-c6cfffff"/>

</lshw:resources>

<lshw:node id="pci:0" claimed="true" class="bridge" handle="PCIBUS:0000:08">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Downstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:07:00.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="ioport" value="5000(size=4096)"/>

<lshw:resource type="memory" value="c6c00000-c6cfffff"/>

</lshw:resources>

<lshw:node id="network" claimed="true" class="network" handle="PCI:0000:08:00.0">

<lshw:description>Ethernet interface</lshw:description>

<lshw:product>VIC Ethernet NIC</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:08:00.0</lshw:businfo>

<lshw:logicalname>eth0</lshw:logicalname>

<lshw:version>a2</lshw:version>

<lshw:serial>84:b8:02:5b:35:7d</lshw:serial>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="autonegotiation" value="off"/>

<lshw:setting id="broadcast" value="yes"/>

<lshw:setting id="driver" value="enic"/>

<lshw:setting id="driverversion" value="2.1.1.50"/>

<lshw:setting id="duplex" value="full"/>

<lshw:setting id="firmware" value="4.1(1.86)"/>

<lshw:setting id="ip" value="172.36.1.181"/>

<lshw:setting id="latency" value="0"/>

<lshw:setting id="link" value="yes"/>

<lshw:setting id="multicast" value="yes"/>

<lshw:setting id="port" value="fibre"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="msix">MSI-X</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

<lshw:capability id="rom">extension ROM</lshw:capability>

<lshw:capability id="ethernet"/>

<lshw:capability id="physical">Physical interface</lshw:capability>

<lshw:capability id="fibre">optical fibre</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="36"/>

<lshw:resource type="memory" value="c6c10000-c6c17fff"/>

<lshw:resource type="memory" value="c6c18000-c6c19fff"/>

<lshw:resource type="ioport" value="5000(size=128)"/>

<lshw:resource type="memory" value="c6c00000-c6c0ffff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="pci:1" claimed="true" class="bridge" handle="PCIBUS:0000:09">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Downstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>1</lshw:physid>

<lshw:businfo>pci@0000:07:01.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="memory" value="c6b00000-c6bfffff"/>

</lshw:resources>

<lshw:node id="fiber" claimed="true" class="bus" handle="PCI:0000:09:00.0">

<lshw:description>Fibre Channel</lshw:description>

<lshw:product>VIC FCoE HBA</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:09:00.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="fnic"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="msix">MSI-X</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="37"/>

<lshw:resource type="memory" value="c6b00000-c6b07fff"/>

<lshw:resource type="memory" value="c6b08000-c6b09fff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="pci:2" claimed="true" class="bridge" handle="PCIBUS:0000:0a">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Downstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>2</lshw:physid>

<lshw:businfo>pci@0000:07:02.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="ioport" value="4000(size=4096)"/>

<lshw:resource type="memory" value="c6a00000-c6afffff"/>

</lshw:resources>

<lshw:node id="network" disabled="true" claimed="true" class="network" handle="PCI:0000:0a:00.0">

<lshw:description>Ethernet interface</lshw:description>

<lshw:product>VIC Ethernet NIC</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:0a:00.0</lshw:businfo>

<lshw:logicalname>eth1</lshw:logicalname>

<lshw:version>a2</lshw:version>

<lshw:serial>84:b8:02:5b:35:7e</lshw:serial>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="autonegotiation" value="off"/>

<lshw:setting id="broadcast" value="yes"/>

<lshw:setting id="driver" value="enic"/>

<lshw:setting id="driverversion" value="2.1.1.50"/>

<lshw:setting id="firmware" value="4.1(1.86)"/>

<lshw:setting id="latency" value="0"/>

<lshw:setting id="link" value="no"/>

<lshw:setting id="multicast" value="yes"/>

<lshw:setting id="port" value="fibre"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="msix">MSI-X</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

<lshw:capability id="rom">extension ROM</lshw:capability>

<lshw:capability id="ethernet"/>

<lshw:capability id="physical">Physical interface</lshw:capability>

<lshw:capability id="fibre">optical fibre</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="38"/>

<lshw:resource type="memory" value="c6a10000-c6a17fff"/>

<lshw:resource type="memory" value="c6a18000-c6a19fff"/>

<lshw:resource type="ioport" value="4000(size=128)"/>

<lshw:resource type="memory" value="c6a00000-c6a0ffff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="pci:3" claimed="true" class="bridge" handle="PCIBUS:0000:0b">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>VIC PCIe Downstream Port</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>3</lshw:physid>

<lshw:businfo>pci@0000:07:03.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="0"/>

<lshw:resource type="memory" value="c6900000-c69fffff"/>

</lshw:resources>

<lshw:node id="fiber" claimed="true" class="bus" handle="PCI:0000:0b:00.0">

<lshw:description>Fibre Channel</lshw:description>

<lshw:product>VIC FCoE HBA</lshw:product>

<lshw:vendor>Cisco Systems Inc</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:0b:00.0</lshw:businfo>

<lshw:version>a2</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="fnic"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="msix">MSI-X</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="32"/>

<lshw:resource type="memory" value="c6900000-c6907fff"/>

<lshw:resource type="memory" value="c6908000-c6909fff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

</lshw:node>

</lshw:node>

</lshw:node>

</lshw:node>

<lshw:node id="pci:3" claimed="true" class="bridge" handle="PCIBUS:0000:0c">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 3</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>3</lshw:physid>

<lshw:businfo>pci@0000:00:03.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="93"/>

</lshw:resources>

</lshw:node>

<lshw:node id="pci:4" claimed="true" class="bridge" handle="PCIBUS:0000:0d">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 3</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>3.2</lshw:physid>

<lshw:businfo>pci@0000:00:03.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="94"/>

<lshw:resource type="ioport" value="6000(size=4096)"/>

<lshw:resource type="memory" value="c6f00000-c70fffff"/>

</lshw:resources>

<lshw:node id="storage" class="storage" handle="PCI:0000:0d:00.0">

<lshw:description>RAID bus controller</lshw:description>

<lshw:product>MegaRAID SAS-3 3316 [Intruder]</lshw:product>

<lshw:vendor>LSI Logic / Symbios Logic</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:0d:00.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="storage"/>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="vpd">Vital Product Data</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="msix">MSI-X</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="ioport" value="6000(size=256)"/>

<lshw:resource type="memory" value="c7040000-c704ffff"/>

<lshw:resource type="memory" value="c7000000-c703ffff"/>

<lshw:resource type="memory" value="c6f00000-c6ffffff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="generic:0" class="generic" handle="PCI:0000:00:05.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Map/VTd\_Misc/System Management</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5</lshw:physid>

<lshw:businfo>pci@0000:00:05.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:1" class="generic" handle="PCI:0000:00:05.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D IIO Hot Plug</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5.1</lshw:physid>

<lshw:businfo>pci@0000:00:05.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:2" class="generic" handle="PCI:0000:00:05.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D IIO RAS/Control Status/Global Errors</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5.2</lshw:physid>

<lshw:businfo>pci@0000:00:05.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:3" class="generic" handle="PCI:0000:00:05.4">

<lshw:description>PIC</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D I/O APIC</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5.4</lshw:physid>

<lshw:businfo>pci@0000:00:05.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="io\_x\_-apic"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="memory" value="c7105000-c7105fff"/>

</lshw:resources>

</lshw:node>

<lshw:node id="generic:4" class="generic" handle="PCI:0000:00:11.0">

<lshw:description>Unassigned class</lshw:description>

<lshw:product>C610/X99 series chipset SPSR</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>11</lshw:physid>

<lshw:businfo>pci@0000:00:11.0</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="storage:0" claimed="true" class="storage" handle="PCI:0000:00:11.4">

<lshw:description>SATA controller</lshw:description>

<lshw:product>C610/X99 series chipset sSATA Controller [AHCI mode]</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>11.4</lshw:physid>

<lshw:businfo>pci@0000:00:11.4</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">66000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="ahci"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="storage"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="ahci\_1.0"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="100"/>

<lshw:resource type="ioport" value="7110(size=8)"/>

<lshw:resource type="ioport" value="7100(size=4)"/>

<lshw:resource type="ioport" value="70f0(size=8)"/>

<lshw:resource type="ioport" value="70e0(size=4)"/>

<lshw:resource type="ioport" value="7020(size=32)"/>

<lshw:resource type="memory" value="c7104000-c71047ff"/>

</lshw:resources>

</lshw:node>

<lshw:node id="communication:0" class="communication" handle="PCI:0000:00:16.0">

<lshw:description>Communication controller</lshw:description>

<lshw:product>C610/X99 series chipset MEI Controller #1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>16</lshw:physid>

<lshw:businfo>pci@0000:00:16.0</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="iomemory" value="3bf0-3bef"/>

<lshw:resource type="memory" value="3bffff13000-3bffff1300f"/>

</lshw:resources>

</lshw:node>

<lshw:node id="communication:1" class="communication" handle="PCI:0000:00:16.1">

<lshw:description>Communication controller</lshw:description>

<lshw:product>C610/X99 series chipset MEI Controller #2</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>16.1</lshw:physid>

<lshw:businfo>pci@0000:00:16.1</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">64</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="iomemory" value="3bf0-3bef"/>

<lshw:resource type="memory" value="3bffff12000-3bffff1200f"/>

</lshw:resources>

</lshw:node>

<lshw:node id="usb:0" claimed="true" class="bus" handle="PCI:0000:00:1a.0">

<lshw:description>USB controller</lshw:description>

<lshw:product>C610/X99 series chipset USB Enhanced Host Controller #2</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1a</lshw:physid>

<lshw:businfo>pci@0000:00:1a.0</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="ehci-pci"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="debug">Debug port</lshw:capability>

<lshw:capability id="ehci">Enhanced Host Controller Interface (USB2)</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="18"/>

<lshw:resource type="memory" value="c7102000-c71023ff"/>

</lshw:resources>

</lshw:node>

<lshw:node id="pci:5" claimed="true" class="bridge" handle="PCIBUS:0000:0e">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>C610/X99 series chipset PCI Express Root Port #1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1c</lshw:physid>

<lshw:businfo>pci@0000:00:1c.0</lshw:businfo>

<lshw:version>d5</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="95"/>

</lshw:resources>

</lshw:node>

<lshw:node id="pci:6" claimed="true" class="bridge" handle="PCIBUS:0000:0f">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>C610/X99 series chipset PCI Express Root Port #8</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1c.7</lshw:physid>

<lshw:businfo>pci@0000:00:1c.7</lshw:businfo>

<lshw:version>d5</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="96"/>

<lshw:resource type="memory" value="c5000000-c68fffff"/>

</lshw:resources>

<lshw:node id="display" class="display" handle="PCI:0000:0f:00.0">

<lshw:description>VGA compatible controller</lshw:description>

<lshw:product>MGA G200e [Pilot] ServerEngines (SEP1)</lshw:product>

<lshw:vendor>Matrox Electronics Systems Ltd.</lshw:vendor>

<lshw:physid>0</lshw:physid>

<lshw:businfo>pci@0000:0f:00.0</lshw:businfo>

<lshw:version>02</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="vga\_controller"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="memory" value="c5000000-c5ffffff"/>

<lshw:resource type="memory" value="c6810000-c6813fff"/>

<lshw:resource type="memory" value="c6000000-c67fffff"/>

<lshw:resource type="memory" value="c6800000-c680ffff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="usb:1" claimed="true" class="bus" handle="PCI:0000:00:1d.0">

<lshw:description>USB controller</lshw:description>

<lshw:product>C610/X99 series chipset USB Enhanced Host Controller #1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1d</lshw:physid>

<lshw:businfo>pci@0000:00:1d.0</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="ehci-pci"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="debug">Debug port</lshw:capability>

<lshw:capability id="ehci">Enhanced Host Controller Interface (USB2)</lshw:capability>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="18"/>

<lshw:resource type="memory" value="c7101000-c71013ff"/>

</lshw:resources>

</lshw:node>

<lshw:node id="isa" claimed="true" class="bridge" handle="PCI:0000:00:1f.0">

<lshw:description>ISA bridge</lshw:description>

<lshw:product>C610/X99 series chipset LPC Controller</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1f</lshw:physid>

<lshw:businfo>pci@0000:00:1f.0</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="lpc\_ich"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="isa"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="0"/>

</lshw:resources>

</lshw:node>

<lshw:node id="storage:1" claimed="true" class="storage" handle="PCI:0000:00:1f.2">

<lshw:description>SATA controller</lshw:description>

<lshw:product>C610/X99 series chipset 6-Port SATA Controller [AHCI mode]</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1f.2</lshw:physid>

<lshw:businfo>pci@0000:00:1f.2</lshw:businfo>

<lshw:version>05</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">66000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="ahci"/>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="storage"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="ahci\_1.0"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="112"/>

<lshw:resource type="ioport" value="7070(size=8)"/>

<lshw:resource type="ioport" value="7060(size=4)"/>

<lshw:resource type="ioport" value="7050(size=8)"/>

<lshw:resource type="ioport" value="7040(size=4)"/>

<lshw:resource type="ioport" value="7000(size=32)"/>

<lshw:resource type="memory" value="c7100000-c71007ff"/>

</lshw:resources>

</lshw:node>

</lshw:node>

<lshw:node id="generic:0" class="generic" handle="PCI:0000:7f:08.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1</lshw:physid>

<lshw:businfo>pci@0000:7f:08.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:1" class="generic" handle="PCI:0000:7f:08.2">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>3</lshw:physid>

<lshw:businfo>pci@0000:7f:08.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:2" class="generic" handle="PCI:0000:7f:08.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4</lshw:physid>

<lshw:businfo>pci@0000:7f:08.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:3" class="generic" handle="PCI:0000:7f:09.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6</lshw:physid>

<lshw:businfo>pci@0000:7f:09.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:4" class="generic" handle="PCI:0000:7f:09.2">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7</lshw:physid>

<lshw:businfo>pci@0000:7f:09.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:5" class="generic" handle="PCI:0000:7f:09.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8</lshw:physid>

<lshw:businfo>pci@0000:7f:09.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:6" class="generic" handle="PCI:0000:7f:0b.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link 0/1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>9</lshw:physid>

<lshw:businfo>pci@0000:7f:0b.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:7" class="generic" handle="PCI:0000:7f:0b.1">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link 0/1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>a</lshw:physid>

<lshw:businfo>pci@0000:7f:0b.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:8" class="generic" handle="PCI:0000:7f:0b.2">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link 0/1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>b</lshw:physid>

<lshw:businfo>pci@0000:7f:0b.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:9" class="generic" handle="PCI:0000:7f:0b.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link Debug</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>c</lshw:physid>

<lshw:businfo>pci@0000:7f:0b.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:10" class="generic" handle="PCI:0000:7f:0c.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>d</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:11" class="generic" handle="PCI:0000:7f:0c.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>e</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:12" class="generic" handle="PCI:0000:7f:0c.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>f</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:13" class="generic" handle="PCI:0000:7f:0c.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>10</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:14" class="generic" handle="PCI:0000:7f:0c.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>11</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:15" class="generic" handle="PCI:0000:7f:0c.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>12</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:16" class="generic" handle="PCI:0000:7f:0c.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>13</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:17" class="generic" handle="PCI:0000:7f:0c.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>14</lshw:physid>

<lshw:businfo>pci@0000:7f:0c.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:18" class="generic" handle="PCI:0000:7f:0f.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>15</lshw:physid>

<lshw:businfo>pci@0000:7f:0f.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:19" class="generic" handle="PCI:0000:7f:0f.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>16</lshw:physid>

<lshw:businfo>pci@0000:7f:0f.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:20" class="generic" handle="PCI:0000:7f:0f.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>17</lshw:physid>

<lshw:businfo>pci@0000:7f:0f.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:21" class="generic" handle="PCI:0000:7f:0f.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>18</lshw:physid>

<lshw:businfo>pci@0000:7f:0f.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:22" class="generic" handle="PCI:0000:7f:0f.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>19</lshw:physid>

<lshw:businfo>pci@0000:7f:0f.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:23" class="generic" handle="PCI:0000:7f:10.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R2PCIe Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1a</lshw:physid>

<lshw:businfo>pci@0000:7f:10.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:24" class="generic" handle="PCI:0000:7f:10.1">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R2PCIe Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1b</lshw:physid>

<lshw:businfo>pci@0000:7f:10.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:25" class="generic" handle="PCI:0000:7f:10.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Ubox</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1c</lshw:physid>

<lshw:businfo>pci@0000:7f:10.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:26" class="generic" handle="PCI:0000:7f:10.6">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Ubox</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1d</lshw:physid>

<lshw:businfo>pci@0000:7f:10.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:27" class="generic" handle="PCI:0000:7f:10.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Ubox</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1e</lshw:physid>

<lshw:businfo>pci@0000:7f:10.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:28" class="generic" handle="PCI:0000:7f:12.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Home Agent 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>1f</lshw:physid>

<lshw:businfo>pci@0000:7f:12.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:29" class="generic" handle="PCI:0000:7f:12.1">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Home Agent 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>20</lshw:physid>

<lshw:businfo>pci@0000:7f:12.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:30" class="generic" handle="PCI:0000:7f:13.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Target Address/Thermal/RAS</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>21</lshw:physid>

<lshw:businfo>pci@0000:7f:13.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:31" class="generic" handle="PCI:0000:7f:13.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Target Address/Thermal/RAS</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>23</lshw:physid>

<lshw:businfo>pci@0000:7f:13.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:32" class="generic" handle="PCI:0000:7f:13.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>24</lshw:physid>

<lshw:businfo>pci@0000:7f:13.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:33" class="generic" handle="PCI:0000:7f:13.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>25</lshw:physid>

<lshw:businfo>pci@0000:7f:13.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:34" class="generic" handle="PCI:0000:7f:13.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>26</lshw:physid>

<lshw:businfo>pci@0000:7f:13.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:35" class="generic" handle="PCI:0000:7f:13.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>27</lshw:physid>

<lshw:businfo>pci@0000:7f:13.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:36" class="generic" handle="PCI:0000:7f:13.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>28</lshw:physid>

<lshw:businfo>pci@0000:7f:13.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:37" class="generic" handle="PCI:0000:7f:13.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Global Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>29</lshw:physid>

<lshw:businfo>pci@0000:7f:13.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:38" class="generic" handle="PCI:0000:7f:14.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 0 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2a</lshw:physid>

<lshw:businfo>pci@0000:7f:14.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:39" class="generic" handle="PCI:0000:7f:14.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 1 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2b</lshw:physid>

<lshw:businfo>pci@0000:7f:14.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:40" class="generic" handle="PCI:0000:7f:14.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 0 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2c</lshw:physid>

<lshw:businfo>pci@0000:7f:14.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:41" class="generic" handle="PCI:0000:7f:14.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 1 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2d</lshw:physid>

<lshw:businfo>pci@0000:7f:14.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:42" class="generic" handle="PCI:0000:7f:14.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2e</lshw:physid>

<lshw:businfo>pci@0000:7f:14.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:43" class="generic" handle="PCI:0000:7f:14.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2f</lshw:physid>

<lshw:businfo>pci@0000:7f:14.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:44" class="generic" handle="PCI:0000:7f:14.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>30</lshw:physid>

<lshw:businfo>pci@0000:7f:14.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:45" class="generic" handle="PCI:0000:7f:14.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>31</lshw:physid>

<lshw:businfo>pci@0000:7f:14.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:46" class="generic" handle="PCI:0000:7f:15.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 2 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>32</lshw:physid>

<lshw:businfo>pci@0000:7f:15.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:47" class="generic" handle="PCI:0000:7f:15.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 3 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>33</lshw:physid>

<lshw:businfo>pci@0000:7f:15.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:48" class="generic" handle="PCI:0000:7f:15.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 2 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>3e</lshw:physid>

<lshw:businfo>pci@0000:7f:15.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:49" class="generic" handle="PCI:0000:7f:15.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 3 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>3f</lshw:physid>

<lshw:businfo>pci@0000:7f:15.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:50" class="generic" handle="PCI:0000:7f:16.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Target Address/Thermal/RAS</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>40</lshw:physid>

<lshw:businfo>pci@0000:7f:16.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:51" class="generic" handle="PCI:0000:7f:16.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>41</lshw:physid>

<lshw:businfo>pci@0000:7f:16.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:52" class="generic" handle="PCI:0000:7f:16.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Global Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>42</lshw:physid>

<lshw:businfo>pci@0000:7f:16.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:53" class="generic" handle="PCI:0000:7f:17.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 1 - Channel 0 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>43</lshw:physid>

<lshw:businfo>pci@0000:7f:17.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:54" class="generic" handle="PCI:0000:7f:17.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>44</lshw:physid>

<lshw:businfo>pci@0000:7f:17.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:55" class="generic" handle="PCI:0000:7f:17.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>45</lshw:physid>

<lshw:businfo>pci@0000:7f:17.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:56" class="generic" handle="PCI:0000:7f:17.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>46</lshw:physid>

<lshw:businfo>pci@0000:7f:17.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:57" class="generic" handle="PCI:0000:7f:17.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>47</lshw:physid>

<lshw:businfo>pci@0000:7f:17.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:58" class="generic" handle="PCI:0000:7f:1e.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>48</lshw:physid>

<lshw:businfo>pci@0000:7f:1e.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:59" class="generic" handle="PCI:0000:7f:1e.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>49</lshw:physid>

<lshw:businfo>pci@0000:7f:1e.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:60" class="generic" handle="PCI:0000:7f:1e.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4a</lshw:physid>

<lshw:businfo>pci@0000:7f:1e.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:61" class="generic" handle="PCI:0000:7f:1e.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4b</lshw:physid>

<lshw:businfo>pci@0000:7f:1e.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:62" class="generic" handle="PCI:0000:7f:1e.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4c</lshw:physid>

<lshw:businfo>pci@0000:7f:1e.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:63" class="generic" handle="PCI:0000:7f:1f.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4d</lshw:physid>

<lshw:businfo>pci@0000:7f:1f.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:64" class="generic" handle="PCI:0000:7f:1f.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4e</lshw:physid>

<lshw:businfo>pci@0000:7f:1f.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="pci:1" claimed="true" class="bridge" handle="PCIBUS:0000:81">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 2</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2</lshw:physid>

<lshw:businfo>pci@0000:80:02.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="98"/>

</lshw:resources>

</lshw:node>

<lshw:node id="pci:2" claimed="true" class="bridge" handle="PCIBUS:0000:82">

<lshw:description>PCI bridge</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D PCI Express Root Port 2</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>2.2</lshw:physid>

<lshw:businfo>pci@0000:80:02.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="driver" value="pcieport"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pci"/>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="normal\_decode"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="irq" value="99"/>

</lshw:resources>

</lshw:node>

<lshw:node id="generic:65" class="generic" handle="PCI:0000:80:05.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Map/VTd\_Misc/System Management</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5</lshw:physid>

<lshw:businfo>pci@0000:80:05.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:66" class="generic" handle="PCI:0000:80:05.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D IIO Hot Plug</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5.1</lshw:physid>

<lshw:businfo>pci@0000:80:05.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="msi">Message Signalled Interrupts</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:67" class="generic" handle="PCI:0000:80:05.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D IIO RAS/Control Status/Global Errors</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5.2</lshw:physid>

<lshw:businfo>pci@0000:80:05.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:68" class="generic" handle="PCI:0000:80:05.4">

<lshw:description>PIC</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D I/O APIC</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5.4</lshw:physid>

<lshw:businfo>pci@0000:80:05.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="pm">Power Management</lshw:capability>

<lshw:capability id="io\_x\_-apic"/>

<lshw:capability id="bus\_master">bus mastering</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

<lshw:resources>

<lshw:resource type="memory" value="fbf00000-fbf00fff"/>

</lshw:resources>

</lshw:node>

<lshw:node id="generic:69" class="generic" handle="PCI:0000:ff:08.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>4f</lshw:physid>

<lshw:businfo>pci@0000:ff:08.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:70" class="generic" handle="PCI:0000:ff:08.2">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>50</lshw:physid>

<lshw:businfo>pci@0000:ff:08.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:71" class="generic" handle="PCI:0000:ff:08.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>51</lshw:physid>

<lshw:businfo>pci@0000:ff:08.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:72" class="generic" handle="PCI:0000:ff:09.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>52</lshw:physid>

<lshw:businfo>pci@0000:ff:09.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:73" class="generic" handle="PCI:0000:ff:09.2">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>53</lshw:physid>

<lshw:businfo>pci@0000:ff:09.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:74" class="generic" handle="PCI:0000:ff:09.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D QPI Link 1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>54</lshw:physid>

<lshw:businfo>pci@0000:ff:09.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:75" class="generic" handle="PCI:0000:ff:0b.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link 0/1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>55</lshw:physid>

<lshw:businfo>pci@0000:ff:0b.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:76" class="generic" handle="PCI:0000:ff:0b.1">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link 0/1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>56</lshw:physid>

<lshw:businfo>pci@0000:ff:0b.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:77" class="generic" handle="PCI:0000:ff:0b.2">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link 0/1</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>57</lshw:physid>

<lshw:businfo>pci@0000:ff:0b.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:78" class="generic" handle="PCI:0000:ff:0b.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R3 QPI Link Debug</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>58</lshw:physid>

<lshw:businfo>pci@0000:ff:0b.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:79" class="generic" handle="PCI:0000:ff:0c.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>59</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:80" class="generic" handle="PCI:0000:ff:0c.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5a</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:81" class="generic" handle="PCI:0000:ff:0c.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5b</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:82" class="generic" handle="PCI:0000:ff:0c.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5c</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:83" class="generic" handle="PCI:0000:ff:0c.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5d</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:84" class="generic" handle="PCI:0000:ff:0c.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5e</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:85" class="generic" handle="PCI:0000:ff:0c.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>5f</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:86" class="generic" handle="PCI:0000:ff:0c.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>60</lshw:physid>

<lshw:businfo>pci@0000:ff:0c.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:87" class="generic" handle="PCI:0000:ff:0f.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>61</lshw:physid>

<lshw:businfo>pci@0000:ff:0f.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:88" class="generic" handle="PCI:0000:ff:0f.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>62</lshw:physid>

<lshw:businfo>pci@0000:ff:0f.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:89" class="generic" handle="PCI:0000:ff:0f.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>63</lshw:physid>

<lshw:businfo>pci@0000:ff:0f.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:90" class="generic" handle="PCI:0000:ff:0f.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>64</lshw:physid>

<lshw:businfo>pci@0000:ff:0f.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:91" class="generic" handle="PCI:0000:ff:0f.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Caching Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>65</lshw:physid>

<lshw:businfo>pci@0000:ff:0f.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:92" class="generic" handle="PCI:0000:ff:10.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R2PCIe Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>66</lshw:physid>

<lshw:businfo>pci@0000:ff:10.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:93" class="generic" handle="PCI:0000:ff:10.1">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D R2PCIe Agent</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>67</lshw:physid>

<lshw:businfo>pci@0000:ff:10.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:94" class="generic" handle="PCI:0000:ff:10.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Ubox</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>68</lshw:physid>

<lshw:businfo>pci@0000:ff:10.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:95" class="generic" handle="PCI:0000:ff:10.6">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Ubox</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>69</lshw:physid>

<lshw:businfo>pci@0000:ff:10.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:96" class="generic" handle="PCI:0000:ff:10.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Ubox</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6a</lshw:physid>

<lshw:businfo>pci@0000:ff:10.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:97" class="generic" handle="PCI:0000:ff:12.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Home Agent 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6b</lshw:physid>

<lshw:businfo>pci@0000:ff:12.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:98" class="generic" handle="PCI:0000:ff:12.1">

<lshw:description>Performance counters</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Home Agent 0</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6c</lshw:physid>

<lshw:businfo>pci@0000:ff:12.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:99" class="generic" handle="PCI:0000:ff:13.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Target Address/Thermal/RAS</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6d</lshw:physid>

<lshw:businfo>pci@0000:ff:13.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:100" class="generic" handle="PCI:0000:ff:13.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Target Address/Thermal/RAS</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6e</lshw:physid>

<lshw:businfo>pci@0000:ff:13.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:101" class="generic" handle="PCI:0000:ff:13.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>6f</lshw:physid>

<lshw:businfo>pci@0000:ff:13.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:102" class="generic" handle="PCI:0000:ff:13.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>70</lshw:physid>

<lshw:businfo>pci@0000:ff:13.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:103" class="generic" handle="PCI:0000:ff:13.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>71</lshw:physid>

<lshw:businfo>pci@0000:ff:13.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:104" class="generic" handle="PCI:0000:ff:13.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel Target Address Decoder</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>72</lshw:physid>

<lshw:businfo>pci@0000:ff:13.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:105" class="generic" handle="PCI:0000:ff:13.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>73</lshw:physid>

<lshw:businfo>pci@0000:ff:13.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:106" class="generic" handle="PCI:0000:ff:13.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Global Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>74</lshw:physid>

<lshw:businfo>pci@0000:ff:13.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:107" class="generic" handle="PCI:0000:ff:14.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 0 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>75</lshw:physid>

<lshw:businfo>pci@0000:ff:14.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:108" class="generic" handle="PCI:0000:ff:14.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 1 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>76</lshw:physid>

<lshw:businfo>pci@0000:ff:14.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:109" class="generic" handle="PCI:0000:ff:14.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 0 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>77</lshw:physid>

<lshw:businfo>pci@0000:ff:14.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:110" class="generic" handle="PCI:0000:ff:14.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 1 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>78</lshw:physid>

<lshw:businfo>pci@0000:ff:14.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:111" class="generic" handle="PCI:0000:ff:14.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>79</lshw:physid>

<lshw:businfo>pci@0000:ff:14.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:112" class="generic" handle="PCI:0000:ff:14.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7a</lshw:physid>

<lshw:businfo>pci@0000:ff:14.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:113" class="generic" handle="PCI:0000:ff:14.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7b</lshw:physid>

<lshw:businfo>pci@0000:ff:14.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:114" class="generic" handle="PCI:0000:ff:14.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 0/1 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7c</lshw:physid>

<lshw:businfo>pci@0000:ff:14.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:115" class="generic" handle="PCI:0000:ff:15.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 2 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7d</lshw:physid>

<lshw:businfo>pci@0000:ff:15.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:116" class="generic" handle="PCI:0000:ff:15.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 3 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7e</lshw:physid>

<lshw:businfo>pci@0000:ff:15.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:117" class="generic" handle="PCI:0000:ff:15.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 2 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>7f</lshw:physid>

<lshw:businfo>pci@0000:ff:15.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:118" class="generic" handle="PCI:0000:ff:15.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 0 - Channel 3 Error</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>80</lshw:physid>

<lshw:businfo>pci@0000:ff:15.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="pciexpress">PCI Express</lshw:capability>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:119" class="generic" handle="PCI:0000:ff:16.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Target Address/Thermal/RAS</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>81</lshw:physid>

<lshw:businfo>pci@0000:ff:16.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:120" class="generic" handle="PCI:0000:ff:16.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>82</lshw:physid>

<lshw:businfo>pci@0000:ff:16.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:121" class="generic" handle="PCI:0000:ff:16.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Global Broadcast</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>83</lshw:physid>

<lshw:businfo>pci@0000:ff:16.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:122" class="generic" handle="PCI:0000:ff:17.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Memory Controller 1 - Channel 0 Thermal Control</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>84</lshw:physid>

<lshw:businfo>pci@0000:ff:17.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="cap\_list">PCI capabilities listing</lshw:capability>

</lshw:capabilities>

</lshw:node>

<lshw:node id="generic:123" class="generic" handle="PCI:0000:ff:17.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>85</lshw:physid>

<lshw:businfo>pci@0000:ff:17.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:124" class="generic" handle="PCI:0000:ff:17.5">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>86</lshw:physid>

<lshw:businfo>pci@0000:ff:17.5</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:125" class="generic" handle="PCI:0000:ff:17.6">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>87</lshw:physid>

<lshw:businfo>pci@0000:ff:17.6</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:126" class="generic" handle="PCI:0000:ff:17.7">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D DDRIO Channel 2/3 Interface</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>88</lshw:physid>

<lshw:businfo>pci@0000:ff:17.7</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:127" class="generic" handle="PCI:0000:ff:1e.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>89</lshw:physid>

<lshw:businfo>pci@0000:ff:1e.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:128" class="generic" handle="PCI:0000:ff:1e.1">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8a</lshw:physid>

<lshw:businfo>pci@0000:ff:1e.1</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:129" class="generic" handle="PCI:0000:ff:1e.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8b</lshw:physid>

<lshw:businfo>pci@0000:ff:1e.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:130" class="generic" handle="PCI:0000:ff:1e.3">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8c</lshw:physid>

<lshw:businfo>pci@0000:ff:1e.3</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:131" class="generic" handle="PCI:0000:ff:1e.4">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8d</lshw:physid>

<lshw:businfo>pci@0000:ff:1e.4</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:132" class="generic" handle="PCI:0000:ff:1f.0">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8e</lshw:physid>

<lshw:businfo>pci@0000:ff:1f.0</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="generic:133" class="generic" handle="PCI:0000:ff:1f.2">

<lshw:description>System peripheral</lshw:description>

<lshw:product>Xeon E7 v4/Xeon E5 v4/Xeon E3 v4/Xeon D Power Control Unit</lshw:product>

<lshw:vendor>Intel Corporation</lshw:vendor>

<lshw:physid>8f</lshw:physid>

<lshw:businfo>pci@0000:ff:1f.2</lshw:businfo>

<lshw:version>01</lshw:version>

<lshw:width units="bits">32</lshw:width>

<lshw:clock units="Hz">33000000</lshw:clock>

<lshw:configuration>

<lshw:setting id="latency" value="0"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="scsi" claimed="true" class="storage" handle="">

<lshw:physid>90</lshw:physid>

<lshw:logicalname>scsi12</lshw:logicalname>

<lshw:node id="generic" class="generic" handle="SCSI:12:00:00:00">

<lshw:description>SCSI</lshw:description>

<lshw:product>Controller</lshw:product>

<lshw:vendor>IET</lshw:vendor>

<lshw:physid>0.0.0</lshw:physid>

<lshw:businfo>scsi@12:0.0.0</lshw:businfo>

<lshw:version>0001</lshw:version>

<lshw:serial>beaf10</lshw:serial>

<lshw:configuration>

<lshw:setting id="ansiversion" value="5"/>

</lshw:configuration>

</lshw:node>

<lshw:node id="disk" claimed="true" class="volume" handle="SCSI:12:00:00:01">

<lshw:description>EXT4 volume</lshw:description>

<lshw:product>VIRTUAL-DISK</lshw:product>

<lshw:vendor>Linux</lshw:vendor>

<lshw:physid>0.0.1</lshw:physid>

<lshw:businfo>scsi@12:0.0.1</lshw:businfo>

<lshw:logicalname>/dev/sda</lshw:logicalname>

<lshw:logicalname>/media/root-ro</lshw:logicalname>

<lshw:dev>8:0</lshw:dev>

<lshw:version>1.0</lshw:version>

<lshw:serial>6872cb1e-3766-4ff3-9de5-11ffd99c3fc9</lshw:serial>

<lshw:size units="bytes">1468006400</lshw:size>

<lshw:configuration>

<lshw:setting id="ansiversion" value="5"/>

<lshw:setting id="created" value="2016-04-18 16:39:57"/>

<lshw:setting id="filesystem" value="ext4"/>

<lshw:setting id="label" value="cloudimg-rootfs"/>

<lshw:setting id="lastmountpoint" value="/tmp/copy-source-to-target.qeycvT/tgt"/>

<lshw:setting id="modified" value="2016-04-18 16:40:52"/>

<lshw:setting id="mount.fstype" value="ext4"/>

<lshw:setting id="mount.options" value="ro,relatime,data=ordered"/>

<lshw:setting id="mounted" value="2016-04-18 16:39:58"/>

<lshw:setting id="sectorsize" value="4096"/>

<lshw:setting id="state" value="mounted"/>

</lshw:configuration>

<lshw:capabilities>

<lshw:capability id="journaled"/>

<lshw:capability id="extended\_attributes">Extended Attributes</lshw:capability>

<lshw:capability id="large\_files">4GB+ files</lshw:capability>

<lshw:capability id="huge\_files">16TB+ files</lshw:capability>

<lshw:capability id="dir\_nlink">directories with 65000+ subdirs</lshw:capability>

<lshw:capability id="extents">extent-based allocation</lshw:capability>

<lshw:capability id="ext4"/>

<lshw:capability id="ext2">EXT2/EXT3</lshw:capability>

<lshw:capability id="initialized">initialized volume</lshw:capability>

</lshw:capabilities>

</lshw:node>

</lshw:node>

</lshw:node>

</lshw:node>

</lshw:list>

</list>