

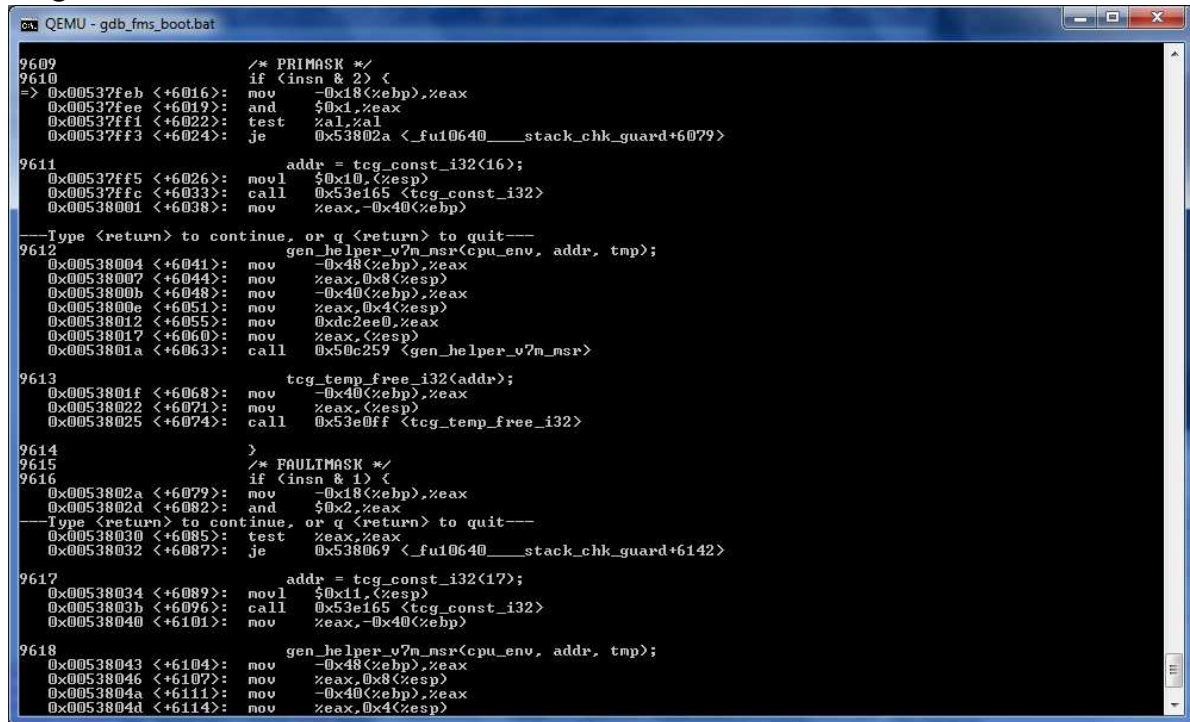
## Mingw bug

Notebook: work

Created: 28.02.2012 5:56

Updated: 28.02.2012 6:01

## original order



```
QEMU - gdb_fms_boot.bat
9609          /* PRIMASK */
9610          if (insn & 2) {
=> 0x00537feb <+6016>: mov     -0x18(%ebp),%eax
0x00537fee <+6019>: and     $0x1,%eax
0x00537ff1 <+6022>: test   %al,%al
0x00537ff3 <+6024>: je     0x53802a <_fu10640___stack_chk_guard+6079>

9611          addr = tcg_const_i32(16);
0x00537ff5 <+6026>: movl   $0x10,(%esp)
0x00537ffc <+6033>: call  0x53e165 <tcg_const_i32>
0x00538001 <+6038>: mov    %eax,-0x40(%ebp)

---Type <return> to continue, or q <return> to quit---
9612          gen_helper_v7m_nsr(cpu_env, addr, tmp);
0x00538004 <+6041>: mov    -0x48(%ebp),%eax
0x00538007 <+6044>: mov    %eax,0x8(%esp)
0x0053800b <+6048>: mov    -0x40(%ebp),%eax
0x0053800e <+6051>: mov    %eax,0x4(%esp)
0x00538012 <+6055>: mov    0xdc2ee0,%eax
0x00538017 <+6060>: mov    %eax,(%esp)
0x0053801a <+6063>: call  0x50c257 <gen_helper_v7m_nsr>

9613          tcg_temp_free_i32(addr);
0x0053801f <+6068>: mov    -0x40(%ebp),%eax
0x00538022 <+6071>: mov    %eax,(%esp)
0x00538025 <+6074>: call  0x53e0ff <tcg_temp_free_i32>

9614          }
9615          /* FAULTMASK */
9616          if (insn & 1) {
0x0053802a <+6079>: mov    -0x18(%ebp),%eax
0x0053802d <+6082>: and     $0x2,%eax
---Type <return> to continue, or q <return> to quit---
0x00538030 <+6085>: test   %eax,%eax
0x00538032 <+6087>: je     0x538069 <_fu10640___stack_chk_guard+6142>

9617          addr = tcg_const_i32(17);
0x00538034 <+6089>: movl   $0x11,(%esp)
0x0053803b <+6096>: call  0x53e165 <tcg_const_i32>
0x00538040 <+6101>: mov    %eax,-0x40(%ebp)

9618          gen_helper_v7m_nsr(cpu_env, addr, tmp);
0x00538043 <+6104>: mov    -0x48(%ebp),%eax
0x00538046 <+6107>: mov    %eax,0x8(%esp)
0x0053804a <+6111>: mov    -0x40(%ebp),%eax
0x0053804d <+6114>: mov    %eax,0x4(%esp)
```

if we will switch order

```
QEMU - gdb_fms_bootbat
0x00537fe8 <+6013>: mov    %eax,-0x48(%ebp)
9609                                     /* FAULTMASK */
---Type <return> to continue, or q <return> to quit---
9610                                     if (insn & 1) {
0x00537feb <+6016>: mov    -0x18(%ebp),%eax
0x00537fee <+6019>: and   $0x1,%eax
0x00537ff1 <+6022>: test  %al,%al
0x00537ff3 <+6024>: je    0x53802a <_fu10640___stack_chk_guard+6079>
9611                                     addr = tcg_const_i32(17);
0x00537ff5 <+6026>: movl  $0x11,(%esp)
0x00537ffc <+6033>: call  0x53e165 <tcg_const_i32>
0x00538001 <+6038>: mov   %eax,-0x40(%ebp)
9612                                     gen_helper_v7m_msr(cpu_env, addr, tmp);
0x00538004 <+6041>: mov   -0x48(%ebp),%eax
0x00538007 <+6044>: mov   %eax,0x8(%esp)
0x0053800b <+6048>: mov   -0x40(%ebp),%eax
0x0053800e <+6051>: mov   %eax,0x4(%esp)
0x00538012 <+6055>: mov   0xdc2ee0,%eax
0x00538017 <+6060>: mov   %eax,(%esp)
0x0053801a <+6063>: call  0x50c259 <gen_helper_v7m_msr>
9613                                     tcg_temp_free_i32(addr);
0x0053801f <+6068>: mov   -0x40(%ebp),%eax
0x00538022 <+6071>: mov   %eax,(%esp)
0x00538025 <+6074>: call  0x53e0ff <tcg_temp_free_i32>
9614                                     }
9615                                     /* PRIMASK */
9616                                     if (insn & 2) {
0x0053802a <+6079>: mov   -0x18(%ebp),%eax
---Type <return> to continue, or q <return> to quit---
0x0053802d <+6082>: and   $0x2,%eax
0x00538030 <+6085>: test  %eax,%eax
0x00538032 <+6087>: je    0x538069 <_fu10640___stack_chk_guard+6142>
9617                                     addr = tcg_const_i32(16);
0x00538034 <+6089>: movl  $0x10,(%esp)
0x0053803b <+6096>: call  0x53e165 <tcg_const_i32>
0x00538040 <+6101>: mov   %eax,-0x40(%ebp)
9618                                     gen_helper_v7m_msr(cpu_env, addr, tmp);
0x00538043 <+6104>: mov   -0x48(%ebp),%eax
0x00538046 <+6107>: mov   %eax,0x8(%esp)
0x0053804a <+6111>: mov   -0x40(%ebp),%eax
0x0053804d <+6114>: mov   %eax,0x4(%esp)
0x00538051 <+6118>: mov   0xdc2ee0,%eax
0x00538056 <+6123>: mov   %eax,(%esp)
0x00538059 <+6126>: call  0x50c259 <gen_helper_v7m_msr>
9619                                     tcg_temp_free_i32(addr);
0x0053805e <+6131>: mov   -0x40(%ebp),%eax
0x00538061 <+6134>: mov   %eax,(%esp)
0x00538064 <+6137>: call  0x53e0ff <tcg_temp_free_i32>
9620                                     }
```